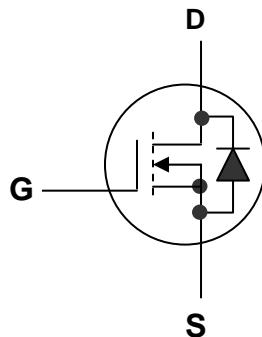
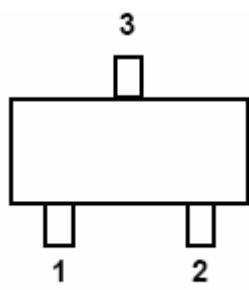


1. Features

- $V_{DS}=20V, R_{DS(ON)}=30m\Omega @ V_{GS}=10V, I_D=6.0A$
- $V_{DS}=20V, R_{DS(ON)}=40m\Omega @ V_{GS}=4.5V, I_D=3.0A$
- $V_{DS}=20V, R_{DS(ON)}=55m\Omega @ V_{GS}=2.5V, I_D=2.0A$

2. Pin information



Pin	Function
1	Gate
2	Source
3	Drain

3. Maximum ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GS}	± 10	V
Drain current-continuous* $T_J=125^\circ C$ pulsed	I_D	6.0	A
Peak drain current	I_{DM}	20	A
Power dissipation*	P_D	1.25	W
Thermal resistance,junction-ambient	R_{thJA}	100	$^\circ C / W$
Operating junction and storage temperature range	T_j, T_{stg}	-55~150	$^\circ C$

*Surface Mounted on FR 4 Board,t≤10 sec.

6. Electrical characteristics

(unless otherwise noted, Ta=25°C)

Characteristic	Symbol	Test condition	Min	Typ	Max	Unit
Drain-source breakdown voltage	V _{DSS}	V _{GS} =0V, I _D =250μA	20	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =16V, V _{GS} =0V	-	-	1.0	μA
Gate-body leakage	I _{GSS}	V _{GS} =±10V, V _{GS} =0V	-	-	±100	nA
Gate threshold voltage*	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5	0.78	1.0	V
Drain-source on-state resistance*	R _{DS(on)}	V _{GS} =10V, I _D =6.0A V _{GS} =2.5V, I _D =3.0A V _{GS} =1.8V, I _D =2.0A	-	28 38 52	30 40 55	mΩ
On-state drain current*	I _{D(on)}	V _{DS} =5V, V _{GS} =4.5V	5	-	-	A
Forward transconductance*	g _{fs}	V _{DS} =15V _{DS(on)} , I _D =5A	30	-	-	S
Input capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	888	-	pF
Output capacitance	C _{oss}		-	144	-	
Reverse transfer capacitance	C _{rss}		-	115	-	
Total gate charge	Q _g	V _{DS} =10V, I _D =3.5A, V _{GS} =4.5V		16.8	-	nC
Gate-source charge	Q _{gs}			2.5	-	
Gate-drain charge	Q _{gd}			5.4	-	
Turn-on delay time	t _{d(on)}	V _{DD} =10V, I _D =1A, R _G =6Ω, R _L =10Ω	-	31.8	-	ns
Rise time	t _r		-	14.5	-	
Turn-off delay time	t _{d(off)}		-	50.3	-	
Fall time	t _f		-	31.9	-	
Drain-source diode forward current*	I _s		-	-	1.25	A
Diode forward voltage	V _{SD}	V _{GS} =0V, I _s =1.25A	-	0.825	1.3	V

7. Package outline

