

## P-Channel Enhancement Mode MOSFET

### Description

The NP2301A uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.

### General Features

- ◆  $V_{DS} = -20V$ ,  $I_D = -2.8A$   
 $R_{DS(ON)}(\text{Typ.}) = 75m\Omega$  @  $V_{GS} = -2.5V$   
 $R_{DS(ON)}(\text{Typ.}) = 60m\Omega$  @  $V_{GS} = -4.5V$
- ◆ High power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface mount package

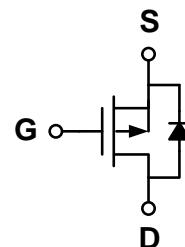
### Application

- ◆ PWM applications
- ◆ Load switch

### Package

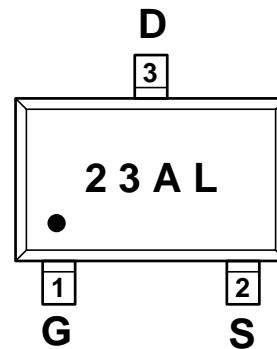
- ◆ SOT-23-3L

### Schematic diagram



### Marking and pin assignment

SOT-23-3L  
(TOP VIEW)



### Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
NP2301AMR	-55°C to +150°C	SOT-23-3L	3000

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

parameter	symbol	limit	unit
Drain-source voltage	$V_{DS}$	-20	V
Gate-source voltage	$V_{GS}$	$\pm 12$	V
Drain current-continuous <sup>a</sup> @Tj=125°C -pulse d <sup>b</sup>	$I_D$	-2.8	A
	$I_{DM}$	-11	A
Drain-source Diode forward current	$I_S$	-1.25	A
Maximum power dissipation	$P_D$	1.2	W
Operating junction Temperature range	$T_j$	-55—150	°C

**Electrical Characteristics** (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>OFF Characteristics</b>						
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-20	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-	-1	μA
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V	-	-	±100	nA
<b>ON Characteristics</b>						
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.4	-0.65	-1.2	V
Drain-source on-state resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.8A	-	60	80	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2.8A	-	75	120	
Forward transconductance	g <sub>f</sub>	V <sub>GS</sub> =-5V, I <sub>D</sub> =-5A	-	5	-	S
<b>Dynamic Characteristics</b>						
Input capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V f=1.0MHz	-	561	-	pF
Output capacitance	C <sub>OSS</sub>		-	61	-	
Reverse transfer capacitance	C <sub>RSS</sub>		-	52	-	
<b>Switching Characteristics</b>						
Turn-on delay time	t <sub>D(ON)</sub>	V <sub>DD</sub> =-10V I <sub>D</sub> =-2.8A V <sub>GEN</sub> =-4.5V R <sub>L</sub> =10ohm R <sub>GEN</sub> =-60ohm	-	12.5	-	ns
Rise time	tr		-	6.6	-	
Turn-off delay time	t <sub>D(OFF)</sub>		-	113	-	
Fall time	tf		-	46.6	-	
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A V <sub>GS</sub> =-4.5V	-	6.1	-	nC
Gate-source charge	Q <sub>gs</sub>		-	1.7	-	
Gate-drain charge	Q <sub>gd</sub>		-	1.2	-	
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =-1.25A	-	-0.81	-1.2	V

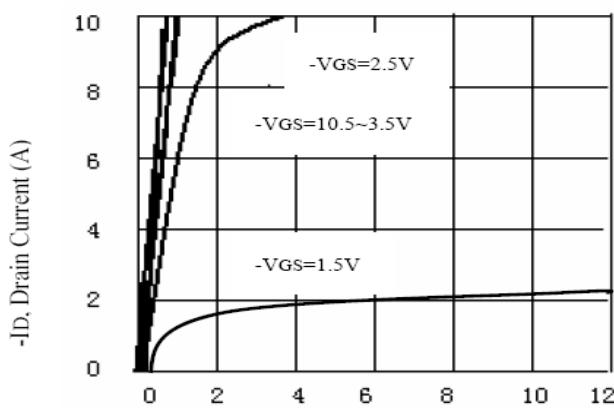
**Notes:**

- surface mounted on FR4 board, t≤10sec
- pulse test: pulse width≤300μs, duty≤2%
- guaranteed by design, not subject to production testing

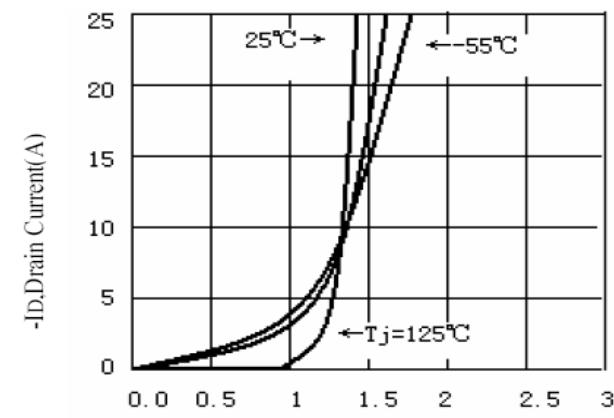
**Thermal Characteristics**

Thermal Resistance junction-to ambient	R <sub>th JA</sub>	100	°C/W
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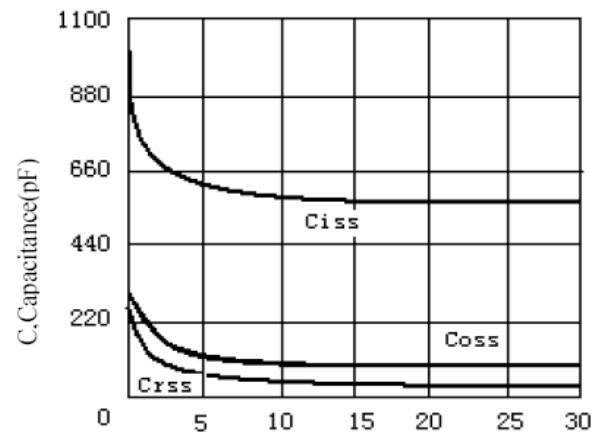
## Typical Performance Characteristics



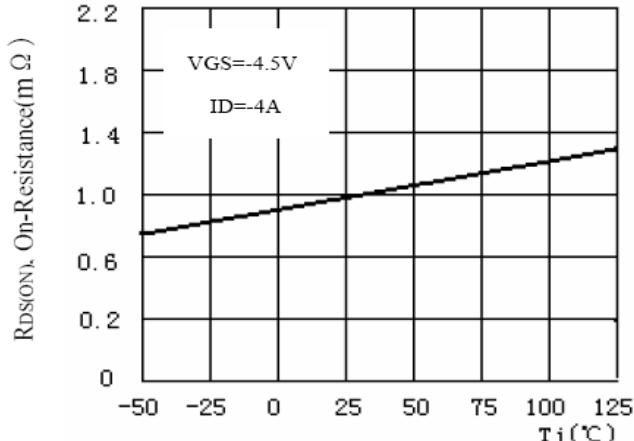
-  $VDS$ , Drain-to-Source Voltage (V)  
 Figure 1. Output Characteristics



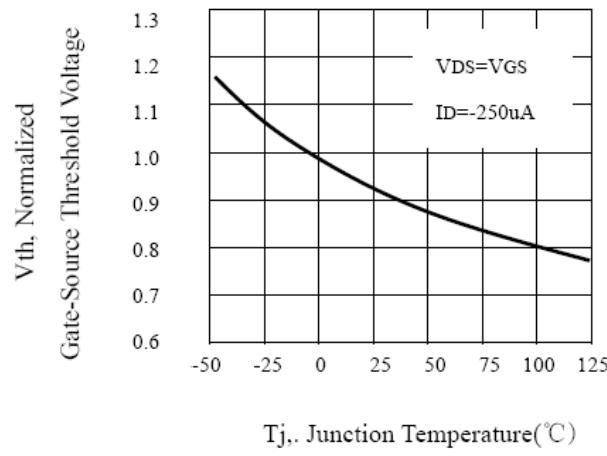
-  $VGS$ , Gate-to-source Voltage (V)  
 Figure 2. Transfer Characteristics



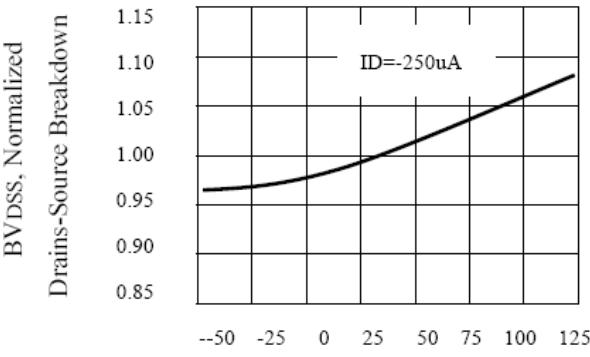
-  $VGS$ , Drain-to Source Voltage  
 Figure3. Capacitance



$R_{DSON}$ , On-Resistance(m Ω)  
 $VGS=-4.5V$   
 $ID=-4A$   
 Figure4. On-Resistance Variation with Temperature



$V_{th}$ , Normalized  
 Gate-Source Threshold Voltage  
 $T_j$ , Junction Temperature(°C)  
 Figure5.Gate Threshold Variation  
 With Temperature



$BV_{DRSS}$ , Normalized  
 Drains-Source Breakdown  
 $T_j$ , Junction Temperature (°C)  
 Figure6.Breakdown Voltage Variation  
 With Temperature

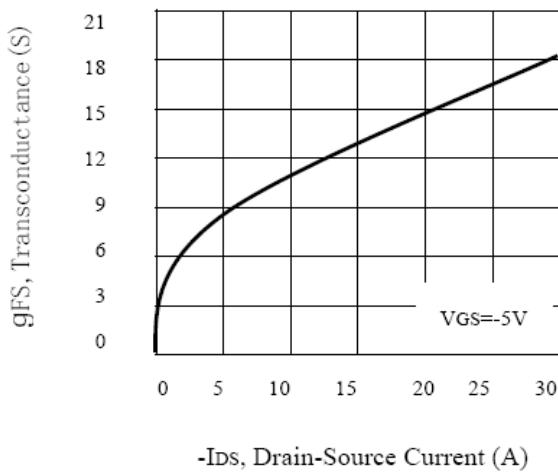


Figure7. Transconductance Variation  
With Drain Current

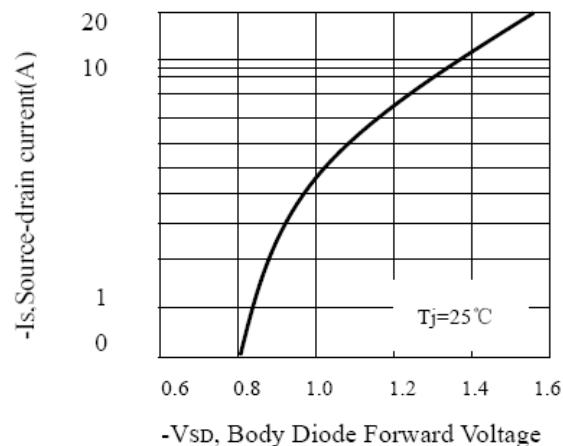


Figure8. Body Diode Forward Voltage  
Variation with Source Current

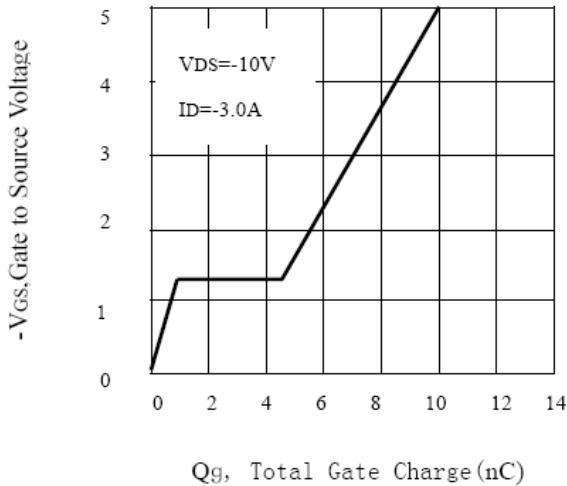


Figure9. Gate Charge

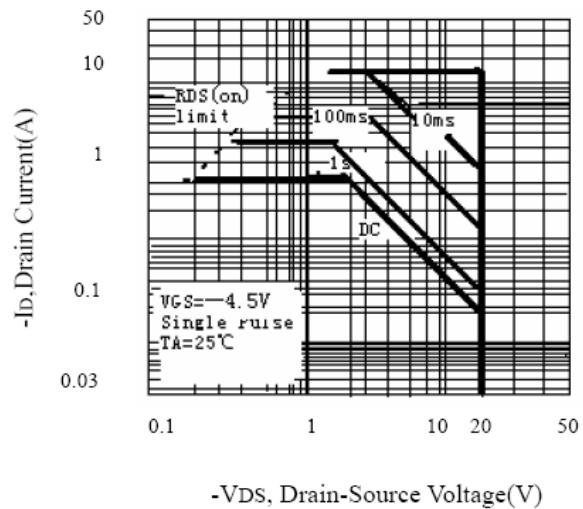
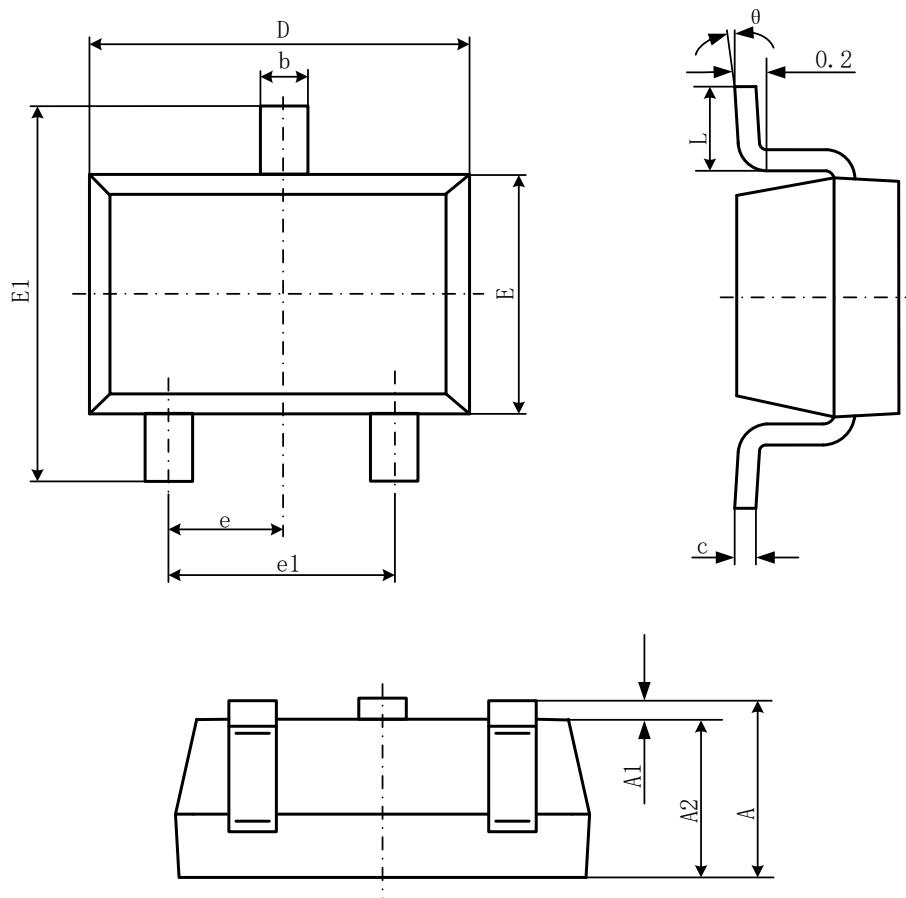


Figure10. Maximum Safe Operating Area

## Package Information

- SOT-23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
theta	0°	8°	0°	8°