## 2SD1628

### **Bipolar Transistor** 20V, 5A, Low VCE(sat), NPN Single PCP

#### Applications

· Strobe DC-DC converters, relay drivers, hammer drivers, lamp drivers, motor drovers

#### Features

- Low saturation voltage
- Large current capacity
- · Very small size making it easy to provide highdensity, small-sized hybrid IC's
- Halogen free compliance

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

	-			
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCEO		20	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		5	А
Collector Current (Pulse)	ICP		8	А

• High hFE

#### Package Dimensions

unit : mm (typ) 7007B-004



# ON Semiconductor® http://onsemi.com

Continued on next page.

#### Product & Package Information

- Package : PCP
- JEITA, JEDEC : SC-62, SOT-89, TO-243
- Minimum Packing Quantity : 1,000 pcs./reel

#### Packing Type: TD

Marking





#### **Electrical Connection**



#### Continued from preceding page.

Parameter Symbol		Conditions	Ratings	Unit	
Collector Dissinction	De		500	mW	
Collector Dissipation	PC	When mounted on ceramic substrate (250mm <sup>2</sup> x0.8mm)	1.5	W	
Junction Temperature	Tj		150	°C	
Storage Temperature	Tstg		-55 to +150	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol		min	typ	max	Unit	
Collector Cutoff Current	ICBO	VCB=50V, IE=0A			100	nA	
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =5V, I <sub>C</sub> =0A			100	nA	
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =2V, I <sub>C</sub> =0.5A	120*		560*		
	h <sub>FE</sub> 2	V <sub>CE</sub> =2V, I <sub>C</sub> =3A	95				
Gain-Bandwidth Product	fŢ	VCE=10V, IC=50mA		120		MHz	
Output Capacitance	Cob	V <sub>CB</sub> =10V, f=1MHz		45		pF	
Collector-to-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	IC=3A, IB=60mA			500	mV	
Base-to-Emitter Saturation Voltage	V <sub>BE</sub> (sat)	IC=3A, IB=60mA			1.5	V	
Turn-ON Time	ton			30		ns	
Storage Time	tstg	See specified Test Circuit.		300		ns	
Fall Time	tf			40		ns	

 $^{\star}$  : The 2SD1628 is classified by 0.5A  $h_{FE}$  as follows :

Rank	Е	F	G		
hFE	120 to 200	160 to 320	280 to 560		

#### Switching Time Test Circuit



 $I_{C}=10I_{B1}=-10I_{B2}=2A$ 

#### **Ordering Information**

Device	Package	Shipping	memo	
2SD1628G-TD-E	PCP	1,000pcs./reel	Pb Free	
2SD1628G-TD-H	PCP	1,000pcs./reel	Pb Free and Halogen Free	
2SD1628F-TD-E	28F-TD-E PCP		Pb Free	
2SD1628F-TD-H	D1628F-TD-H PCP		Pb Free and Halogen Free	







#### Bag Packing Specification 2SD1628G-TD-E, 2SD1628G-TD-H, 2SD1628F-TD-E, 2SD1628F-TD-H

1. Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format			
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)		(C-1)	Outer BOX (A-7)
РСР	PCP	1,000	4,000	24,000	4 ree	els containe	d	6 inner boxes contained
					Dime	nsions:mm (	external)	Dimensions:mm (external)
					18	$3 \times 72 \times$	185	440×195×210
			Reel	label, [	nner	box label		box label
Packing met	h o d			(u 1	nit:r	nm)	The form	label at the time of factory shipments. n of a label may change in physical ntion process.
0			<	6	59	>	<	108
	Type LOT Quan Orig Reel la	No. tity in	-> (1' -> (2' (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z)	II IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII		0 1 1 1 1 1 1 1 1 1 1 1 1 1	YPE CODE
				Label		JEITA	Phase	
				LEAD FRE	EE 3	JEITA P	hase 3A	

LEAD FREE 4

JEITA Phase 3

#### 2. Taping configuration

2-1. Carrier tape size (unit:mm)



#### **Outline Drawing**

#### Land Pattern Example

2SD1628G-TD-E, 2SD1628G-TD-H, 2SD1628F-TD-E, 2SD1628F-TD-H





ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal