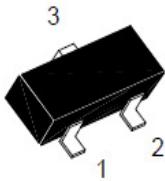


N-Channel High Density Trench MOSFET

◆ Features

1. Advanced Trench Process Technology.
2. High Density Cell Design for Ultra Low On-Resistance.
3. RoHS Compliant.

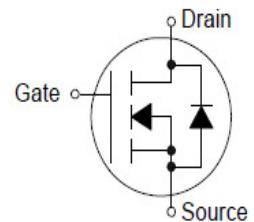
PRODUCT SUMMARY		
V _{DSS}	I _D	R _{D(on)} (m-ohm) Max
30V	4.0A	37 @ VGS = 10V, ID=4.0A
		49 @ VGS = 4.5V, ID=3.5A



SM2306 Pin Assignment & Symbol

3-Lead Plastic SOT-23

Pin 1: Gate 2: Source 3: Drain



◆ Ordering Information

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
SM2306SRL	SM2306SRG	SOT-23	G	S	D	Tape Reel
SM2306X X X		(1) S: SOT-23; (2) R: Tape Reel (3) G: Halogen Free; L: Lead Free				
(1) Package Type	(2) Packing Type	(3) Lead Free				

◆ Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current (Continuous) ^a	4	A
I_{DM}	Drain Current (Pulsed) ^b	20	A
P_D	Total Power Dissipation @ $T_A=25^\circ\text{C}$	0.9	W
T_j, T_{stg}	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient (PCB mounted) ^c	100	$^\circ\text{C}/\text{W}$

a:Fused current that based on wire numbers and diameter

b:Repetitive Rating: Pulse width limited by the maximum junction temperature

c:1-in² 2oz Cu PCB board

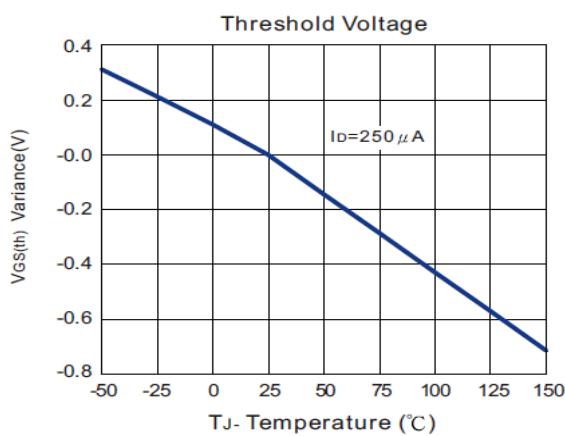
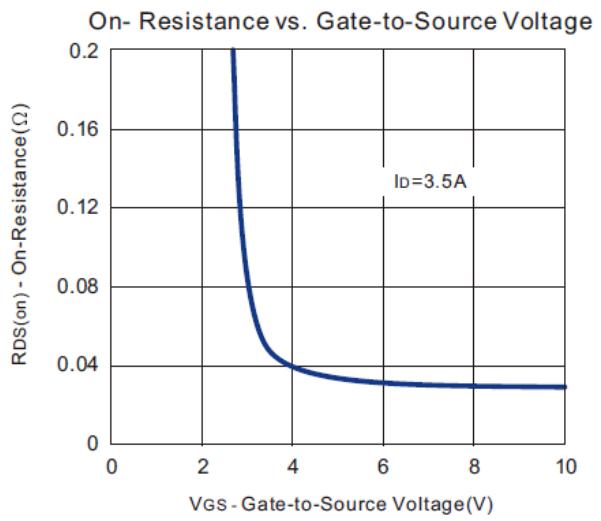
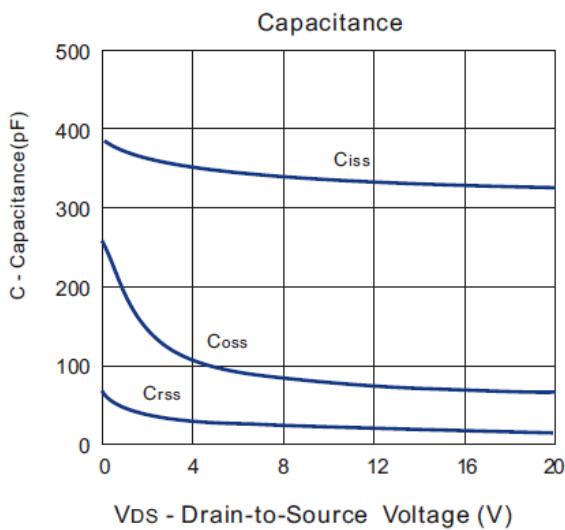
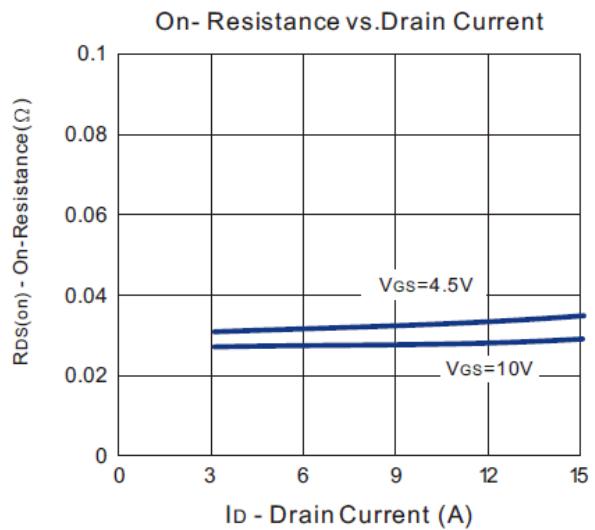
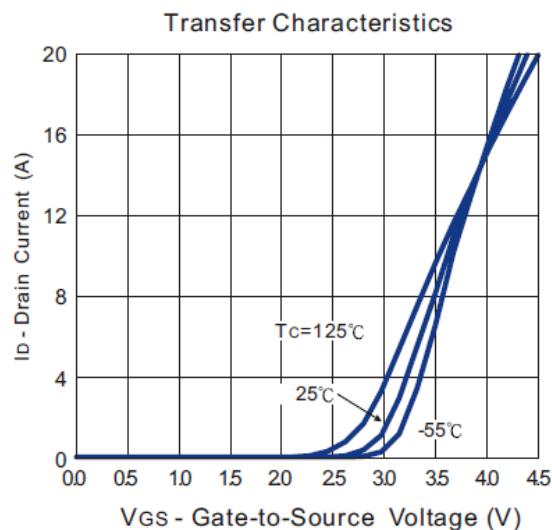
◆ Electrical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	-	-	± 100	nA
• On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1	-	3	V
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=4.0\text{A}$	-	27	37	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=3.5\text{A}$	-	35	49	
• Dynamic Characteristics^d						
C_{iss}	Input Capacitance	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	-	240	-	pF
C_{oss}	Output Capacitance		-	110	-	
C_{rss}	Reverse Transfer Capacitance		-	17	-	
• Switching Characteristics^d						
Q_g	Total Gate Charge	$V_{DS}=15\text{V}, I_D=5.8, V_{GS}=10\text{V}$	-	4.5	-	nC
Q_{gs}	Gate-Source Charge		-	1.2	-	
Q_{gd}	Gate-Drain Charge		-	1.7	-	
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=15\text{V}, R_G=6\Omega, I_D=1\text{A}, V_{GEN}=10\text{V}, R_L=15\Omega$	-	8	-	nS
t_r	Turn-on Rise Time		-	12	-	
$t_{d(off)}$	Turn-off Delay Time		-	17	-	
t_f	Turn-off Fall Time		-	8	-	
• Drain-Source Diode Characteristics						
I_s	Maximum Diode Forward Current	$V_{GS}=0\text{V}, I_s=1\text{A}$	-	-	2.5	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS}=0\text{V}, I_s=1\text{A}$	-	0.76	1	V

Note: Pulse Test: Pulse Width $\leq 300\text{us}$, Duty Cycle $\leq 2\%$

d: Guaranteed by design: not subject to production testing

◆ Characteristics Curve



◆ Characteristics Curve

