Serial No.: AL180320118-00



**PRODUCT TYPE Quartz Crystal HSX321S NOMINAL FREQUENCY** 20.00000MHz H.ELE. SAMPLE O/N EOS-I30189-1 H.ELE. P/N X3S020000BF1HA-V **RELEASE DATE** 2018/03/20 **VERSION** 00 **MSL** Level 1 **☑** Pb free **☑** RoHS Compliant **GREEN PRODUCT ☑** HF-Halogen free **☑** REACH Compliant **CUSTOMER P/N APPLICATION & MODEL** APPROVED BY CUSTOMER (DATE)

| Harmony Electronics Corp.                                   |  |
|---|--|
| F. S. TSAI C. C. HOU U. F. CHEN (APPROVE) (CHECK) (PREPARE) | Country of Origin: Taiwan Factory<br>Thailand Factory<br>China Factory |

#### HARMONY ELECTRONICS CORPRATION

KAOHSIUNG TAIPEI THAILAND SHENZHEN



| REV. No. | DATE       | REASON | REVISE CONTENTS |
|----------|------------|--------|-----------------|
| 0        | 2018/03/20 | New    |                 |
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# **HARMONY ELECTRONICS CORPRATION**



# **Contents**

| ITEM                                 | PAGE |
|--------------------------------------|------|
| 1. QUARTZ CRYSTAL UNIT SPECIFICATION | 3    |
| 2. DIMENSIONS                        | 4    |
| 3. MARKING                           | 4    |
| 4. INSIDE STRUCTURE                  | 5    |
| 5. HANDLING SUGGESTIONS              | 5-6  |
| 6. EMBOSS CARRIER TAPE&REEL          | 6-7  |
| 7. PACKAGE                           | 7-8  |
| 8. MECHANICAL PERFORMANCE            | 8-9  |
| 9. ENVIRONMENTAL PERFORMANCE         | 9    |
| 10. APPENDIX                         |      |

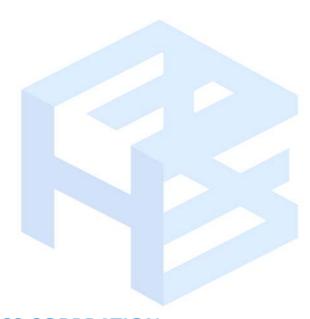
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## 1. QUARTZ CRYSTAL UNIT SPECIFICATION

|                               | Ele                            | ctrical Spe | c.  |          |              |  |
|-------------------------------|--------------------------------|-------------|-----|----------|--------------|--|
| Items                         | Min                            | Туре        | Max | Unit     | Notes        |  |
| 1. Frequency (FL)             |                                | 20.000000   | )   | MHz      |              |  |
| 2. Mode of oscillation:       | F                              | undament    | al  |          |              |  |
| 3. Frequency tolerance        | -10                            |             | +10 | ppm      | at 25°C±3°C  |  |
| 4. Equivalent resistance (RR) |                                |             | 80  | Ω        | SERIES       |  |
| 5. Storage temperature range  | -40                            |             | +85 | °C       |              |  |
| 6. Operable temperature range | -40                            |             | +85 | °C       |              |  |
| 7. Temperature stability      | -30                            |             | +30 | ppm      | -40°C ∼+85°C |  |
| 8. Loading capacitance (CL)   |                                | 15.0        |     | pF       |              |  |
| 9. Drive level (DL)           |                                | 10          | 100 | μW       |              |  |
| 10. Shunt Capacitance (CO)    |                                |             | 2.0 | pF       |              |  |
| 11. Insulation resistance     | 500                            |             |     | МΩ       | at DC 100V   |  |
| 12. Aging:                    | -3                             |             | +3  | ppm/Year |              |  |
| 13. Circuit:                  | Measured in HP/E5100A,S&A 250B |             |     |          |              |  |

Note:



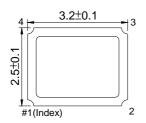
# **HARMONY ELECTRONICS CORPRATION**

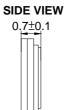
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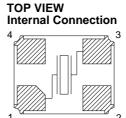


#### 2. DIMENSION

**TOP VIEW** 

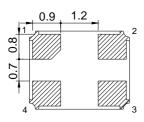






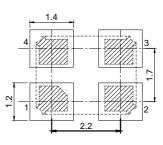
| Pin                         |         |  |  |  |  |
|-----------------------------|---------|--|--|--|--|
| #1                          | Crystal |  |  |  |  |
| #2                          | GND/Lid |  |  |  |  |
| #3                          | Crystal |  |  |  |  |
| #4                          | GND/Lid |  |  |  |  |
| #2 #4 CND(connection Cover) |         |  |  |  |  |

**BOTTOM VIEW** 



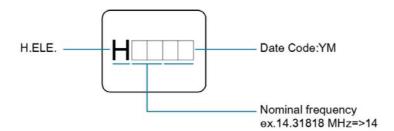
Tolerance: ±0.1 Unit: mm

#### TOP VIEW Land Pattern Layout



\* Note: The Index mark was defined by the BASE suppliers.

#### 3. MARKING



#### Note:

- 1. Laser marking.
- 2. Date Code:

| V-Voor | 2010 | 2011 | 2012 | 2013 | 2014 | 2015 | 2016 | 2017 | 2018 | 2019 |
|--------|------|------|------|------|------|------|------|------|------|------|
| Y=Year | 2020 | 2021 | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 |
| Code   | 0    | 1    | 2    | 3    | 4    | 5    | 6    | 7    | 8    | 9    |

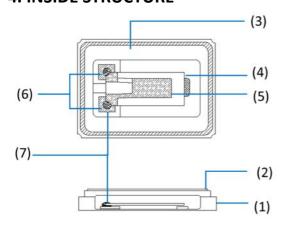
| M=Month | Jan. | Feb. | Mar. | Apr. | May. | Jun. | Jul. | Aug. | Sep. | Oct. | Nov. | Dec. |
|---------|------|------|------|------|------|------|------|------|------|------|------|------|
| Code    | Α    | В    | С    | D    | E    | F    | G    | Н    | 1    | K    | L    | М    |

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#### 4. INSIDE STRUCTURE



Reference drawing

| No. | Component              | Material         | Note                          |
|-----|------------------------|------------------|-------------------------------|
| (1) | Base                   | Ceramic          | $Al_2O_3$                     |
| (2) | Lid                    | Metal            | Fe- Ni –Co<br>Ni Plating      |
| (3) | Kovar                  | Metal            | Fe- Ni -Co                    |
| (4) | Crystal<br>Blank       | Quartz           | SiO₂<br>Rectangular At-Cut    |
| (5) | Electrode              | Metal            | -                             |
| (6) | PAD                    | Metal            | W<br>Ni Plating<br>Au Plating |
| (7) | Connective<br>Adhesive | Silver<br>Powder | Ag                            |

**XThe use prohibition chemistry substance of Table 1 of DHE-0204-1 (HE-QA-24) is not included in this item.** 

#### 5. HANDLING SUGGESTION

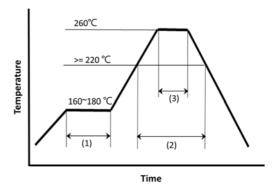
#### **■** Reflow Conditions

Please stay with our proposed reflow conditions and do soldering within 2 times.

| (1) | Preheat      | 160~18 | 0deg.C | 120 sec.     |
|-----|--------------|--------|--------|--------------|
| (2) | Primary heat | >=220  | deg.C  | 60~120 sec.  |
| (3) | Peak         | 260    | deg.C  | 10 sec. Max. |

#### ■ Manual Solder iron (Example)

Bit temp.: 350°C max., Time: 3sec max., Each terminal only should be soldered once.



#### **■** Mounting Conditions

Our products are suitable for most automated SMT processes. However, we strongly advise all our customers to conduct SMT sampling prior to mass production in order to make sure production processes will not affect the properties and specifications of our product. Seal welding and mounting procedures involving the use of ultra-sonic processes are not recommended and will affect and/or damage the internal properties of our product. Excessive shock during the mounting process will also affect the product and we strongly recommend setting SMT conditions to minimize such conditions.

If a possibility of the PCB being warped exists we strongly advise to ensure the degree of warping will not affect the product.

Please also ensure the operating characteristics and or soldering conditions are all within the specifications of use for our product.

Ultimately the worst case scenario of all the above will lead to cases of non-oscillation but other negative effects are also likely should our products be used in an inappropriate way. Please note such cases of misuse and its related quality issues are not included in our product warranty.

### HARMONY ELECTRONICS CORPRATION



#### Cleansing Conditions

General cleaning solutions may be used to clean our products but we always recommend testing to be performed prior to mass production processes. Ultrasonic cleaning procedures are not recommended and we strongly advise other forms of cleansing to be evaluated first. Unsuitable cleansing may lead to a number of negative effects such as damage to the product surface, discoloration of the product, corrosion of the package, package contamination, illegible marking, etc. Please note cases of unadvised treatment and its related quality issues are not included in our product warranty.

#### ■ Storage Conditions

Please ensure our products are preserved appropriately in their original packaging. Irregular environmental instances of moisture will affect our product's stability and may cause problems such as frequency instability, soldering ability and conditions, package defects, and other problems. It is essential to keep our products in a clean dust-free environment out of direct sunlight.

Our products' storage conditions should at least meet the following condition:

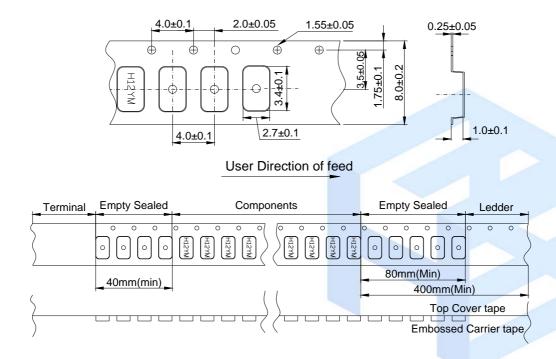
Environmental Temperature: + 85 degrees Celsius Maximum

Relative Humidity: 80% Maximum

Please note storage instances which do not conform to our guidelines and the related quality issues produced as an outcome are not included in our product warranty.

#### 6. EMBOSS CARRIER TAPE AND REEL

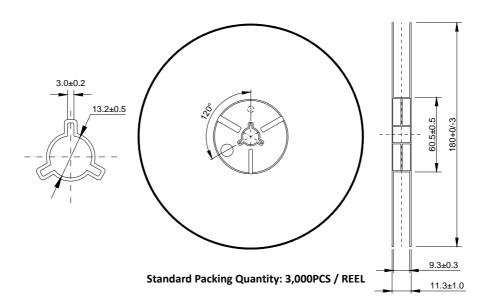
#### **■** Carrier Tape



#### HARMONY ELECTRONICS CORPRATION



#### ■ Reel



#### ■ Material of The Tape

| Tape         | Material      |  |  |  |
|--------------|---------------|--|--|--|
| Carrier tape | PS Conductive |  |  |  |
| Top tape     | PET           |  |  |  |

#### ■ Joint of tape

The carrier-tape and top cover-tape should not be jointed.

#### ■ Release strength of cover tape

The force should be controlled between 0.1N to 0.7N under following condition.

Pulling direction: 165° to 180°

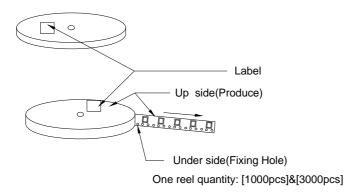
Speed: 300mm/min.

Otherwise unless specified.



Other standards shall be based on JIS C 0806-1990.

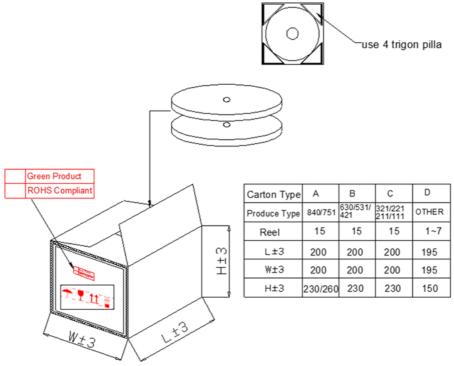
#### 7. PACKAGE





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- ①Top and bottom with 2.3cm thickness foam-rubber cushion for protection.
- ②Carton's Q'TY:1~15 pcs.
- ③Carton Type=A,B,C use 4 trigon pillar to fasten the Reel.
- 4Need to add 3 pages dry agent in each outer box.

#### 8. MECHANICAL PERFORMANCE

|   | Item             | Test M  | ethods                                      | Specifications<br>Code |
|---|------------------|---|---|------------------------|
| 1 | Shock            | Dropping from 120 cm height 3 tim<br>Refer to: JIS C 60068-2-32   | Α   |                        |
| 2 | Vibration        | Frequency 10-55Hz, Sine Wave full 3 axes, 2 cycles and duration of 2 h Refer to: JIS C 60068-2-6  | А   |                        |
| 3 | Leakage Test     | Leak Rate 1.0x10 <sup>-9</sup> Pa-m <sup>3</sup> /sec. Max. detector.   |   |                        |
| 4 | Solder ability   | After applying ROSIN Flux, dipping for 3 ±0.5 sec. Refer to: JIS C 60068-2-20   | in solder bath at 245deg.C ±5deg.C          | В                      |
| 5 | Bending Strength | Mount a sample on board. Apply Pressure to the center of board until it is bent to 3 mm and hold for 5 ±1 sec. Pressure speed: 0.5 mm/sec. Refer to: EIAJ ET-7403 | Pressure rod  20  R230  Board  45± 2  45± 2 | A                      |

## HARMONY ELECTRONICS CORPRATION

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| 6 | Adhesion      | Mount a sample on the circuit board. Apply pressure vertically to the side of specimen attached to the circuit board with the pressure jig.  Pressure: 5N for 10±1 sec.  Refer to: EIAJ ET-7403 | Scratch tool R0.5 Chip cross section | С |
|---|---------------|---|--------------------------------------|---|
| 7 | Body strength | Apply pressure to the center of<br>body with the R0.5 pressure jig.<br>pressure :10N for 10±1sec<br>Refer to: EIAJ ET-7403  | Pressure and R0.5 Chip               | С |

#### 9. ENVIRONMENT PERFORMANCE

|   | Item                            | Test Methods   | Specifications<br>Code |
|---|---------------------------------|--|------------------------|
| 1 | Resistance of<br>Soldering Heat | Performing as the following reflow:  260°C  >= 220 °C  100-180 °C  120s  | A                      |
| 2 | Humidity                        | Temperature $60^{\circ}\text{C}\pm2^{\circ}\text{C}$ , RH $90^{\sim}95\%$ , duration of 240 hours.<br>Back to room temperature first, then check the component after $1^{\sim}2$ hours.<br>Refer to: JIS C $60068-2-3$                                 | А                      |
| 3 | Storage in Low<br>Temperature   | -40deg.C ±2deg.C, duration of 240 hours.  Back to the room temperature first, then check the component after 1~2 hours.  Refer to: JIS C 60068-2-1   | Α                      |
| 4 | Storage in High<br>Temperature  | +85deg.C ±2deg.C, duration of 240 hours.  Back to the room temperature first, then check the component after 1~2 hours.  Refer to: JIS C 60068-2-2   | A                      |
| 5 | Temperature cycles              | -40deg.C $\pm 2$ deg.C (30min) $\leftrightarrow \pm 85$ deg.C $\pm 2$ deg.C (30min) 25 cycles. And Temperature Increasing/reducing time $< 3$ mins. Back to the room temperature first, then check the component after 1~2 hours. Refer to: JIS C 0025 | A                      |

| Specifications code | Specifications  |  |
|---------------------|---|--|
| Α                   | FREQ. DRIFT: $\pm 5$ ppm Max, RESISTANCE DRIFT: $\pm 15\%$ or $2\Omega$   |  |
| В                   | More than 90% of lead shall be covered by new solder.                     |  |
| С                   | FREQ. DRIFT: $\pm 15$ ppm Max, RESISTANCE DRIFT: $\pm 25\%$ or $10\Omega$ |  |

## HARMONY ELECTRONICS CORPRATION

KAOHSIUNG TAIPEI THAILAND SHENZHEN

# FACTORY LOCATION HEAD OFFICE/TAIWAN FACTORY

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#### **CHINA FACTORY**

JU YUAN INDUSTRIAL PARK, QIAO TANG ROAD, TANG WEI COMMUNITY, FUYONG, BAOAN DISTRICT, SHEN ZHEN CITY, CHINA (Post Code:518103).

#### **THAILAND FACTORY**

66MOO 5, KAONGU-BEOKPRAI RD., T.BEOKPRAI, A. BANPONG, RATCHABURI PROVINCE 70110, THAILAND.

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