

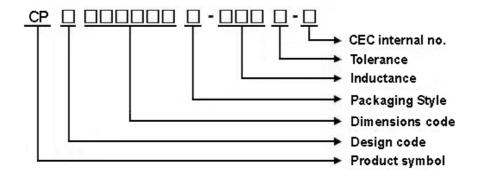
ISO9001 & ISO14001 & TS16949 CHILISIN ELECTRONICS CORP.

RoHS & Halogen Free & REACH Compliance.

SPECIFICATION FOR APPROVAL

Customer : Customer P/N: Drawing No : Quantity : Chilisin P/N :	0 Pcs. Date :	EDIES ND
Chilisin P/N .	CP11000001-3	ENIES-NP
	SPECIFICATION ACCEPTED BY:	
COMPONENT ENGINEER		
ELECTRICAL ENGINEER		
MECHANICAL ENGINEER		
APPROVED		
REJECTED		
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奇力新電子(河南)有限公 Chilisin Electronics (Henan) Co XiuWu Xian, industry gathering JiaoZuo, Henan China Postal Code:454350 TEL:+86-391-717-0666	D., Ltd. 可刀新电寸(鮴外 Chilisin Electronics (Suzhou) Co., Ltd. Rd., Suzhou New District,
Drawn by 張瑞 滿 rammi	Checked by 邱明傑 Joseph.Chiu	Approved by JACKY鍾 jacky.chung

- 1 Scope: This specification applies to Multilayer Ferrite chip inductors
- 2 Part Numbering:



3 Rating:

Operating Temperature: $-40 \,^{\circ}\text{C} \sim 105 \,^{\circ}\text{C}$ (Including self - temperature rise)

Storage Temperature: $-4.0 \,^{\circ}\text{C} \sim 8.5 \,^{\circ}\text{C}$ (after PCB)

 $-5\,^\circ\!\!\mathrm{C}\!\sim\!4\,\,0\,^\circ\!\!\mathrm{C}$,Humidity $\,4\,\,0\,\%\!\!\sim\!7\,\,0\,\%$ (before PCB)

4 Marking:

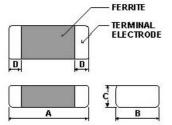
No Marking

5 Standard Testing Condition

Unless otherwise specified		In case of doubt
Temperature	Ordinary Temperature(15 to 35℃)	20±2 ℃
Humidity	Ordinary Humidity(25 to 85% RH)	60 to 70 % RH



6 Configuration and Dimensions:



 Dimensions in mm

 TYPE
 CP160808

 A
 1.6±0.2

 B
 0.8±0.2

 C
 0.8±0.2

 D
 0.3±0.2

7 Electrical Characteristics:

Part No.	Inductance (uH)	Test Freq.	RDC (Ω)Max.	Rated Current (mA)Max.	Tolerance (±%)
CPY160808T-1R0□-NP	1	1 MHz,200 mV	0.12	1500	20,30
CPY160808T-2R2□-NP	2.2	1 MHz,200 mV	0.2	1000	20,30
CPY160808T-4R7□-NP	4.7	1 MHz,200 mV	0.25	800	20,30
CPY160808T-100□-NP	10	1 MHz,200 mV	0.9	90	20,30

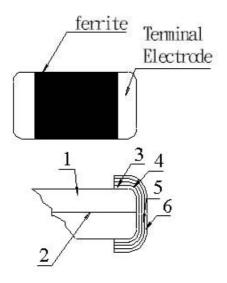
NOTE: □-tolerance M=±20% / T=±30%

^{1.}Operating temperature range $-4~0~{\rm ^{\circ}C} \sim 1~0~5~{\rm ^{\circ}C}$ (Including self - temperature rise)

[&]quot;-N" FOR COMPLETELY LEAD FREE TYPE(INCLUDING FERRITE BODY & SOLDER)



8 CPY160808T Series 8.1 Construction:



8.2 Material List:

NO	PART	MATERIAL		
1	Ferrite Substance	NiO-CuO-ZnO-Ferrite		
2	Silver electrode	Ag Ag		
3	Silver electrode			
4	Cu plating	Cu		
5	Ni plating	Ni		
6	Sn plating	Sn		



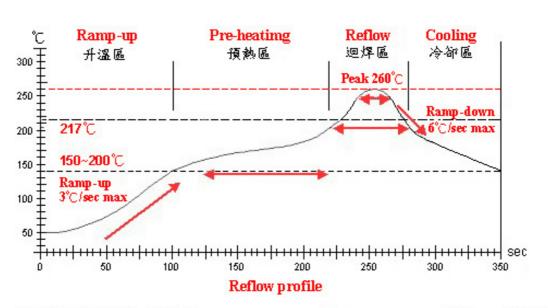
9 Reliability Of Ferrite Multilayer Chip Inductor 1-1.Mechanical Performance

No	Item	Specification	Test Method
1-1-1	Flexure Strength	The forces applied on the right	Test device shall be soldered on the substrate
		conditions must not damage	Substrate Dimension: 100x40x1.6mm
		the terminal electrode and the	Deflection: 2.0mm
		ferrite	Keeping Time: 30sec
			*For 100505, substrate dimension is 100x40x0.8mm
1-1-2	Vibration		Test device shall be soldered on the substrate
			Oscillation Frequency: 10 to 55 to 10Hz for 1min
			Amplitude: 1.5mm
			Time: 2hrs for each axis (X, Y & Z), total 6hrs
1-1-3	Resistance to Soldering Heat	Appearance: No damage	Pre-heating: 150℃, 1min
		More than 75% of the terminal	Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free)
		electrode should be covered	Solder Temperature: 260±5°C
		with solder.	Immersion Time: 10±1sec
		Inductance: within ±20% of	
		initial value	
1-1-4	Solder ability	The electrodes shall be at	Pre-heating: 150℃, 1min
		least 95% covered with new	Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free)
		solder coating	Solder Temperature: 245±5°C (Pb-Free)
			Immersion Time: 4±1sec

1-2.Environmental Performance

No	Item	Specification	Test Method				
1-2-1	Temperature Cycle	Appearance: No damage	One cycle:	One cycle:			
		Inductance:within±20% of	Step	Temperature ($^{\circ}$ C)	Time (min)		
		initial value	1	-40±3	30		
			2	25±2	3		
			3	105±3	30		
			4	25±2	3		
			Total: 100cycles Measured after exposure in the room condition for 2				
1-2-2	Humidity Resistance		Temperatu	Temperature: 40±2°C			
			Relative Humidity: 90 ~ 95% / Time: 1000 Measured after exposure in the room cond				
1-2-3	High		Temperature: 85±3°C				
	Temperature Resistance	Relative Humidity: 20%					
			Applied Cu	rrent: Rated Current / Time: 1	000hrs		
			Measured after exposure in the room condition for 2				
1-2-4	Low		Temperature: -40±3°C				
	Temperature Resistance		Relative Humidity: 0% / Time: 1000hrs				
			Measured after exposure in the room condition for				





Lead-Free(LF) 標準溫度分析範圍

Refer to J-STD-020C

管制項目 Item.	升溫區 Ramp-up	預熱區 Pre-heatimg	迴焊區 Reflow	Peak Temp	冷部區 Cooling
溫度範圍 Temp.scope	R.T. ~150°C	150°C ~ 200°C	217℃	260±5°C	Peak Temp. ~ 150°C
標準時間 Time spec.	_	60 ~ 180 sec	60 ~ 150 sec	20 ~ 40 sec	-
實際時間 Time result	_	75 ~ 100 sec	90 ~ 120 sec	20 ~ 35 sec	/ -

NOTE:

- 1. Re-flow possible times: within 2 times
- 2. Nitrogen adopted is recommended while in re-flow



11 Packaging:

11.1 Packaging -Cover Tape

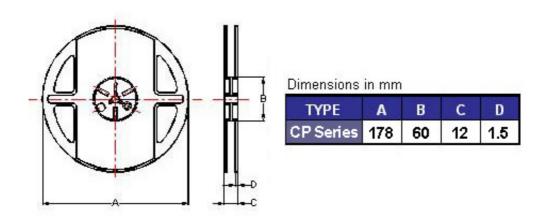
The force for tearing off cover tape is 10 to 100 grams in the arrow direction.



11.2 Packaging Quantity

TYPE	BULK	PCS/REEL
CP160808	~	4000
CP201209	✓	4000
CP201212	/	3000
CP 32 1611	√	3000

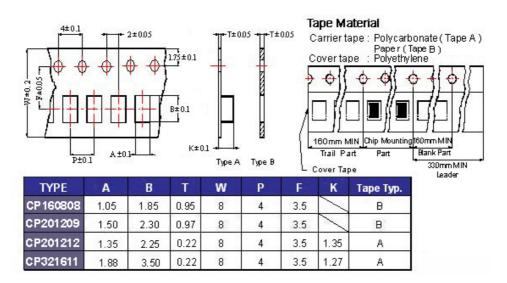
11.3 Reel Dimensions



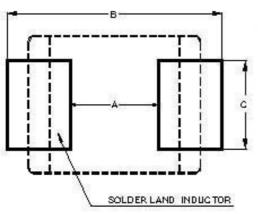


11 Packaging:

11.4 Tape Dimensions in mm



12 Recommended Land Pattern:



Dimensions in mm

TYPE	Α	В	С
CP160808	0.7 ~ 0.8	1.8 ~ 2.0	0.6 ~ 0.8
CP201209	1.0 ~ 1.2	2.6 ~4.0	1.0~1.2
CP201212	1.0 ~ 1.2	2.6 ~4.0	1.0~1.2
CP321611	2.0	4.2~5.2	1.2

13 Note:

- 1. Please make sure that your product has been evaluated and confirmed against your specifications when our product is mounted to your product.
- 2. Do not knock nor drop.
- 3. All the items and parameters in this product specification have been prescribed on the premise that our product is used for the purpose,under the condition and in the environment agreed upon between you and us. You are requested not to use our product deviating from such agreement.
- 4. Please keep the distance between transformer/coil and other components (refer to the standard IEC 950)