SPECIFICATIONS

Customer	
Product Name	Balanced Filter
Sunlord Part Number	SLFBF21-2R450G-A02TF
Customer Part Number	
[New Released, Revised	SPEC No.: SLFB02130000

[This SPEC is total 7 pages including specifications and appendix.]

[ROHS, Halogen-Free and SVHC Compliant Parts]

Approved By	Checked By	Issued By

Shenzhen Sunlord Electronics Co., Ltd.

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[For Customer approval Only]		Date:			
Qualification Status:	Full	Restricted	Rejected		
Approved By	Verified By	Re-checked By	Checked By		
Comments:					

[Version change history]

Rev.	Effective Date	Changed Contents	Change reasons	Approved By
01	1	New release	1	Hai Guo

Scope 1.

This specification applies to SLFBF21-2R450G-A02TF of Balanced Filter.

2. Product Description and Identification (Part Number)

- 1) Description:
 - Multi-layer Chip Balanced Filter
- Product Identification (Part Number) 2)



Center Frequency			Series Code
2R450G 2450.0 MHz			A02
		1	
Packing			Hazardous Substance Free Products
T Tape Carrier Package			F

Electrical Characteristics 3.

Part Number	SLFBF21-2R450G-A02TF		
Unbalance Port Impedance	50 ohm		
Balance Port Impedance	Conjugate match to MTK MT6612/6616		
Frequency Range(BW)	2450.0±50.0 MHz		
Incertion Less in DW/	2.8dB max. at 25		
Insertion Loss in BW	3.1 dB max. at -40 to +85		
	40 dB min at 880~960MHz		
Attonuction (Absolute value)	30 dB min at 1710~1880MHz		
Attenuation(Absolute value)	16 dB min at 1880~1990MHz		
	17 dB min at 4800~5000MHz		
Unbalance Port V.S.W.R in BW	2.10 max.		
Balance Port V.S.W.R in BW	2.10 max.		
Power Capacity	500 mW max.		

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Operating and storage temperature range (individual chip without packing): -40 ~+85 . a)

b) Storage temperature range (packaging conditions): -10 ~ +40 and RH 70% (Max.).

- Test equipment: Network Analyzer:E5071C. C)
- Electrical Performance: See Fig. 3-1, Fig. 3-2 d)



4. Shape and Dimensions

1) Dimensions and terminal configuration: See Fig. 4-1





0.90±0.10

(1):Unbalance Port (2):NC(DC feed Port) (3):NC (4)(6)(8):GND (5)(7):Balance Port M:MARK

Fig. 4-1

LAND Fig. 4-2

5. Test and Measurement Procedures

2) Recommended Land Pattern: See Fig.4-2

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- a. Ambient Temperature: 20±15
- b. Relative Humidity: 65±20%
- c. Air Pressure: 86 KPa to 106 KPa
- If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: 20±2
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86KPa to 106 KPa

5.2 Visual Examination

a. Inspection Equipment: 20 X magnifier

5.3 Reliability Test

Items	Requirements	Test Methods and Remarks		
5.3.1 Terminal Strength	No visible mechanical damage.	Solder the inductor to the testing jig (glass epoxy board shown as the following figure) using eutectic solder. Then apply a force in the direction of the arrow. 10N force for 2012 series. Keep time: 10±1sec. Chip Chip I0N/10±1s Speed: 1.0mm/s Glass Epoxy Board Mounting Pad		

nlord	Specifications f	or Balanced Filter Page
5.3.2 Resistance to Flexure	No visible mechanical damage.	Solder the chip to the test jig (glass epoxy board) using eutectic solder. Then apply a force in the direction sho as the following figure. Solder the chip to the test jig (g epoxy board) using eutectic solder. Then apply a force the direction. Flexure: 2mm Pressurizing Speed: 0.5mm/sec Keep time: ≥30 sec
	Unit: mm R1	20 10 0 1 1 Flexure: 2
5.3.3 Vibration	No visible mechanical damage.	Solder the chip to the testing jig (glass epoxy board sh as the following figure) using eutectic solder. The chip shall be subjected to a simple harmonic motio having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 55 Hz. The frequency range from 10 to 55 Hz and return to 10 shall be traversed in approximately 1 minute. This mot shall be applied for a period of 2 hours in each 3 mutua perpendicular directions (total of 6 hours).
5.3.4 Dropping	No visible mechanical damage.	Drop the chip 10 times on a concrete floor from a height of 100 cm.
5.3.5 Solderability	No visible mechanical damage. Wetting shall be exceeded 75% coverage.	Solder temperature: 240±2 Duration: 3sec Solder: Sn/3.0Ag/0.5Cu Flux: 25% Resin and 75% ethanol in weight
5.3.6 Resistance to Soldering Heat	No visible mechanical damage.	Solder temperature: 260±5 Duration: 5 sec Solder: Sn/3.0Ag/0.5Cu Flux: 25% Resin and 75% ethanol in weight The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.3.7 Thermal Shock	No visible mechanical damage. Satisfy electrical Characteristic.	Temperature and time: -40 for $30\pm 3 \text{ min} \rightarrow 85$ for $30\pm 3 \text{ min}$ Transforming interval: Max. 20 sec. Tested cycle: 100 cycles The chip shall be stabilized at normal condition for 1~2 hours before measuring. 30 min. 85 Ambient Temperature -40 30 min. 30 min. 30 min. 30 min. 30 min.

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Specifications for Balanced Filter

Page 6 of 7

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5.3.8	No visible mechanical	Temperature: 60±2
Damp Heat	damage.	Humidity: 90% to 95% RH
(Steady States)	Satisfy electrical	Duration: 500 ⁺²⁴ hours
	Characteristic.	The chip shall be stabilized at normal condition for 1~2
		hours before measuring.
5.3.9	No visible mechanical	Temperature: 85±2
Resistance to High temperature	damage.	Duration: 500 ⁺²⁴ hours
	Satisfy electrical	The chip shall be stabilized at normal condition for 1~2
	Characteristic.	hours before measuring.

6. Packaging and Storage

6.1 Packaging

There is one type of packaging for the Balanced Filters. Please specify the packing code when ordering.

- 6.1.1 Tape Carrier Packaging:
 - Packaging code: T
 - a. Tape carrier packaging are specified in attached figure Fig. 6.1-1~3
 - b. Tape carrier packaging quantity please see the following table:

Туре	2012[0805]
Таре	Embossed Tape
Quantity	4K

(1) Taping Drawings (Unit: mm)

Embossed Tape



Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)



Fig. 6.1-2

Туре	Chip Thickness	А	В	K max	T max
SLFBF21	0.95±0.10	1.42±0.10	2.25±0.10	1.14	0.27

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(3) Reel Dimensions (Unit: mm)



6.2 Storage

- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40 or less and 70% RH or less.
- The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H₂S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.3.6** shall be guaranteed for 6 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 6 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

7.1 Reflow Profile

Preheat condition: 150 ~200 /60~120sec. Allowed time above 217 : 60~90sec. Max temp: 260 Max time at max temp: 10sec. Solder paste: Sn/3.0Ag/0.5Cu Allowed Reflow time: 2x max

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]

7.2 Iron Soldering Profile

Iron soldering power: Max.30W Pre-heating: 150 / 60 sec. Soldering Tip temperature: 350 Max. Soldering time: 3 sec Max. Solder paste: Sn/3.0Ag/0.5Cu Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]

8. Supplier Information

- a) Supplier:
- Shenzhen Sunlord Electronics Co., Ltd.
- b) Manufacturer:
- Shenzhen Sunlord Electronics Co., Ltd.
- c) Manufacturing Address:

Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China Zip: 518110



