

AP15P03Q

P-Channel Power MOSFET

• General Description

The AP15P03Q combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDS(ON) . This device is ideal for load switch and battery protection applications.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

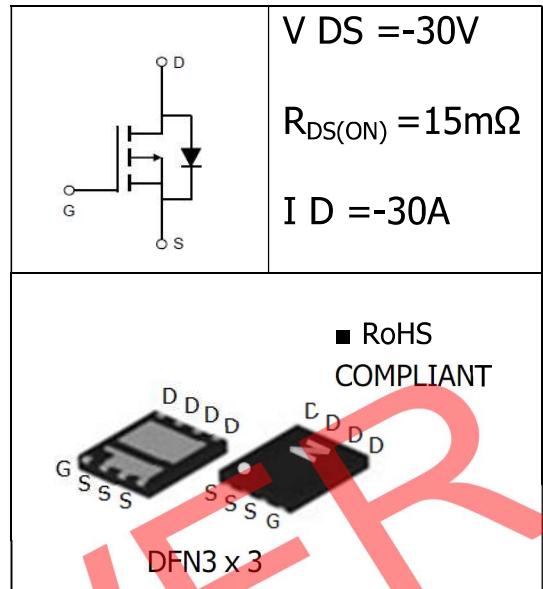
- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

• Ordering Information:

| | |
|---------------------------------------|-----------------|
| Marking | 15P03 |
| Packing | REEL TAPE |
| Basic ordering unit (pcs) | 5000 |
| Normal Package Material Ordering Code | AP15P03Q-TAP |
| Halogen Free Ordering Code | AP15P03Q-TAP-HF |

• Absolute Maximum Ratings ($T_c =25^\circ C$)

| Parameter | Symbol | Rating | Unit |
|--|---------------------|------------|------------|
| Drain-Source Voltage | V_{DS} | -30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Continuous Drain Current($T_c=25^\circ C$) | I_D | -30 | A |
| Pulsed Drain Current ^① | I_{DM} | -60 | A |
| Total Power Dissipation ^② | $P_D@TC=25^\circ C$ | 17 | W |
| Total Power Dissipation | $P_D@TA=25^\circ C$ | 0.9 | W |
| Operating Junction Temperature | T_J | -55 to 150 | $^\circ C$ |
| Storage Temperature | T_{STG} | -55 to 150 | $^\circ C$ |
| Single Pulse Avalanche Energy | E_{AS} | 100 | mJ |



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•Thermal resistance

| Parameter | Symbol | Min. | Typ. | Max. | Unit |
|--|-------------------|------|------|------|-------|
| Thermal resistance, junction - case ⁽²⁾ | R _{thJC} | - | - | 34 | ° C/W |
| Thermal resistance, junction - ambient | R _{thJA} | - | - | 180 | ° C/W |
| Soldering temperature, wavesoldering for 10s | T _{sold} | - | - | 265 | ° C |

•Electronic Characteristics

| Parameter | Symbol | Condition | Min. | Typ | Max. | Unit |
|-----------------------------------|---------------------|---|------|-----|------|------|
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =-250uA | -30 | | | V |
| Gate Threshold Voltage | V _{GS(TH)} | V _{GS} =V _{DS} , I _D =-250uA | -1.2 | | -2.5 | V |
| Drain-Source Leakage Current | I _{DSS} | V _{DS} =-30V, V _{GS} =0V | | | -1.0 | uA |
| Gate- Source Leakage Current | I _{GSS} | V _{GS} =±12V ,V _{DS} =0V | | | ±100 | nA |
| Static Drain-source On Resistance | R _{DS(ON)} | V _{GS} =-10V, I _D =-9A | 15 | 20 | | mΩ |
| | | V _{GS} =-4.5V, I _D =-8A | 25 | 32 | | mΩ |
| Forward Transconductance | g _{FS} | V _{DS} =-10V, I _D =-5A | | 9 | | s |

•Electronic Characteristics

| Parameter | Symbol | Condition | Min. | Typ | Max. | Unit |
|------------------------------|------------------|-----------|------|------|------|------|
| Input capacitance | C _{iss} | f = 1MHz | - | 1650 | - | pF |
| Output capacitance | C _{oss} | | - | 330 | - | |
| Reverse transfer capacitance | C _{rss} | | - | 220 | - | |

•Gate Charge characteristics(T_a = 25°C)

| Parameter | Symbol | Condition | Min. | Typ | Max. | Unit |
|----------------------|-----------------|----------------------------------|------|-----|------|------|
| Total gate charge | Q _g | VDD =25V ID = 8A VGS = 10V | - | 15 | - | nC |
| Gate - Source charge | Q _{gs} | | - | 4 | - | |
| Gate - Drain charge | Q _{gd} | | - | 6 | - | |

Note: ① Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2% ;

② Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;

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Fig.1 Power Dissipation Derating Curve

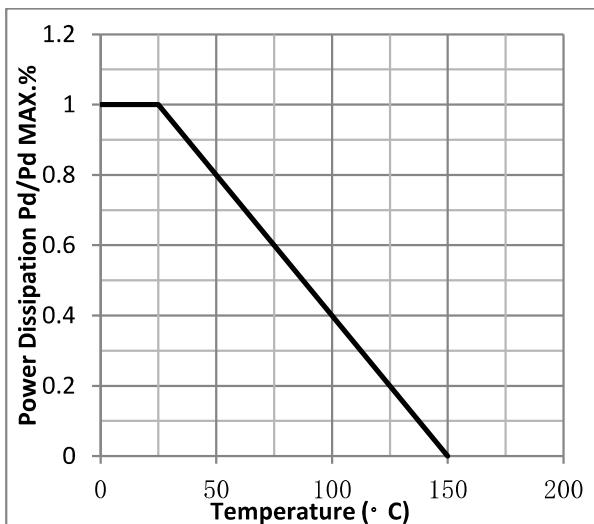


Fig.2 Typical output Characteristics

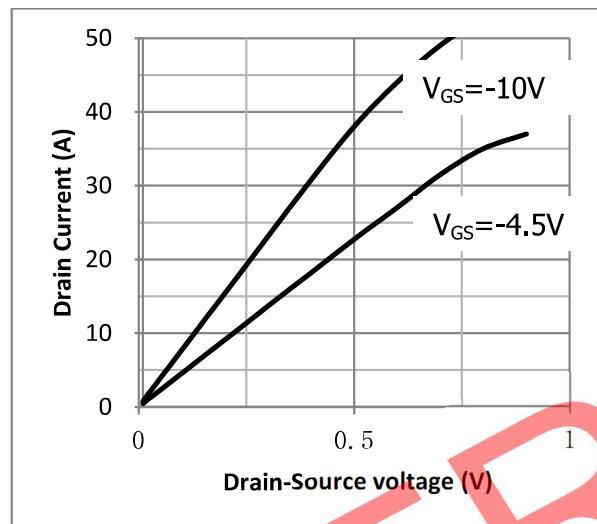


Fig.3 Threshold Voltage V.S Junction Temperature

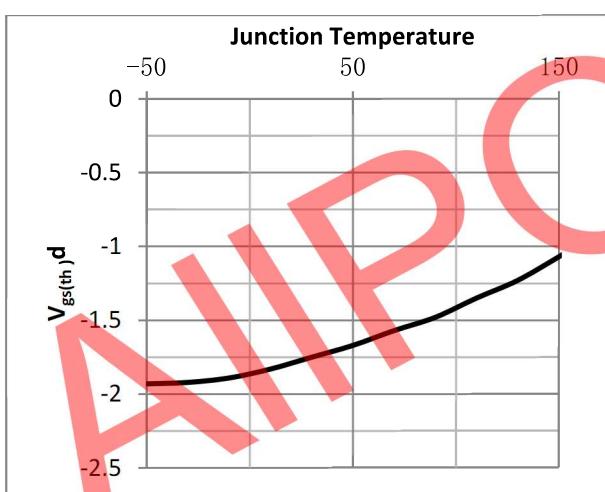


Fig.4 Resistance V.S Drain Current

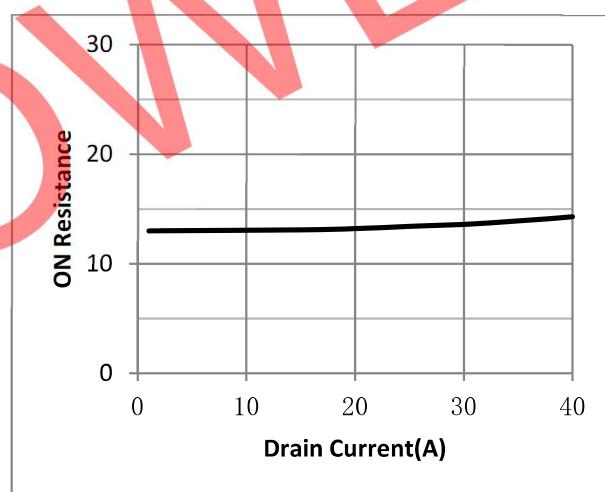


Fig.5 On-Resistance VS Gate Source Voltage

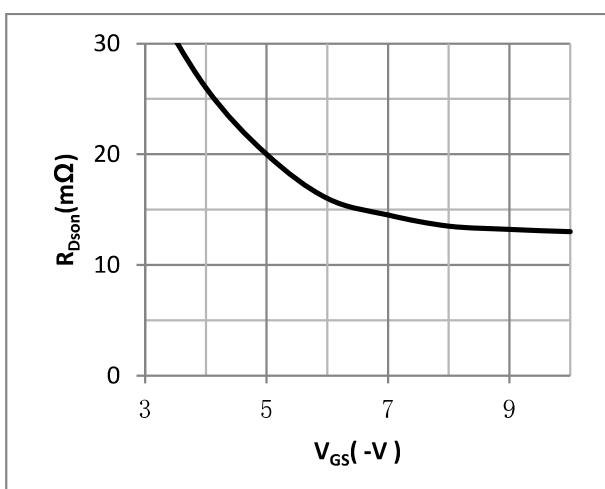
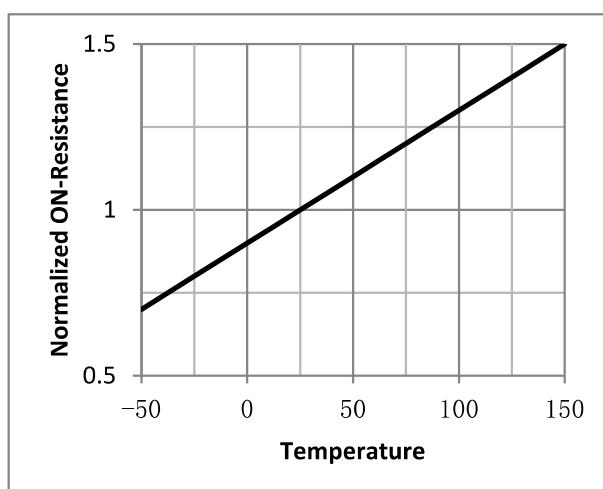


Fig.6 On-Resistance V.S Junction Temperature



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Fig.7 Switching Time Measurement Circuit

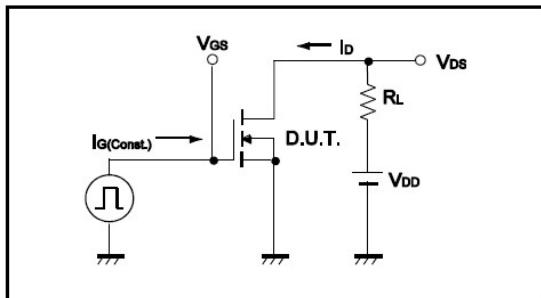


Fig.8 Gate Charge Waveform

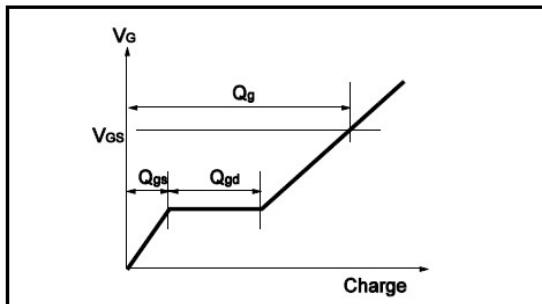


Fig.9 Switching Time Measurement Circuit

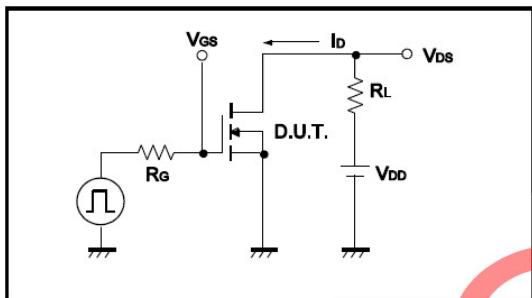


Fig.10 Gate Charge Waveform

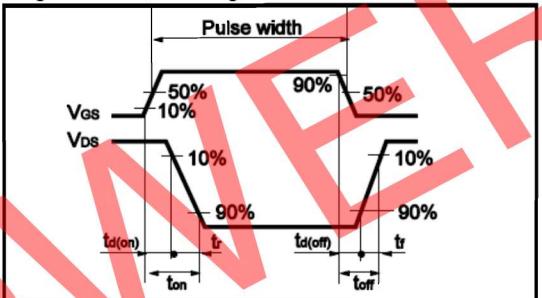


Fig.11 Avalanche Measurement Circuit

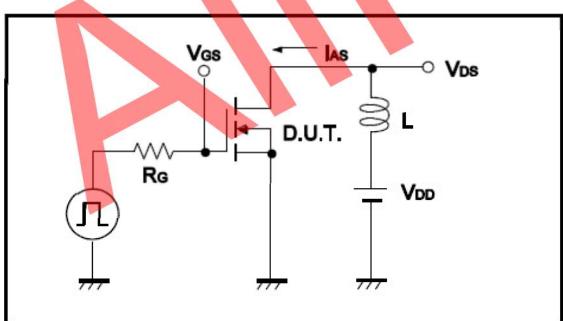
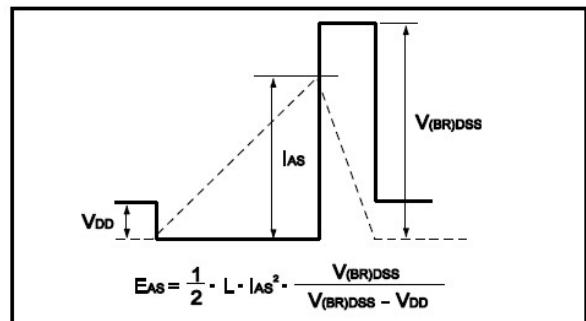


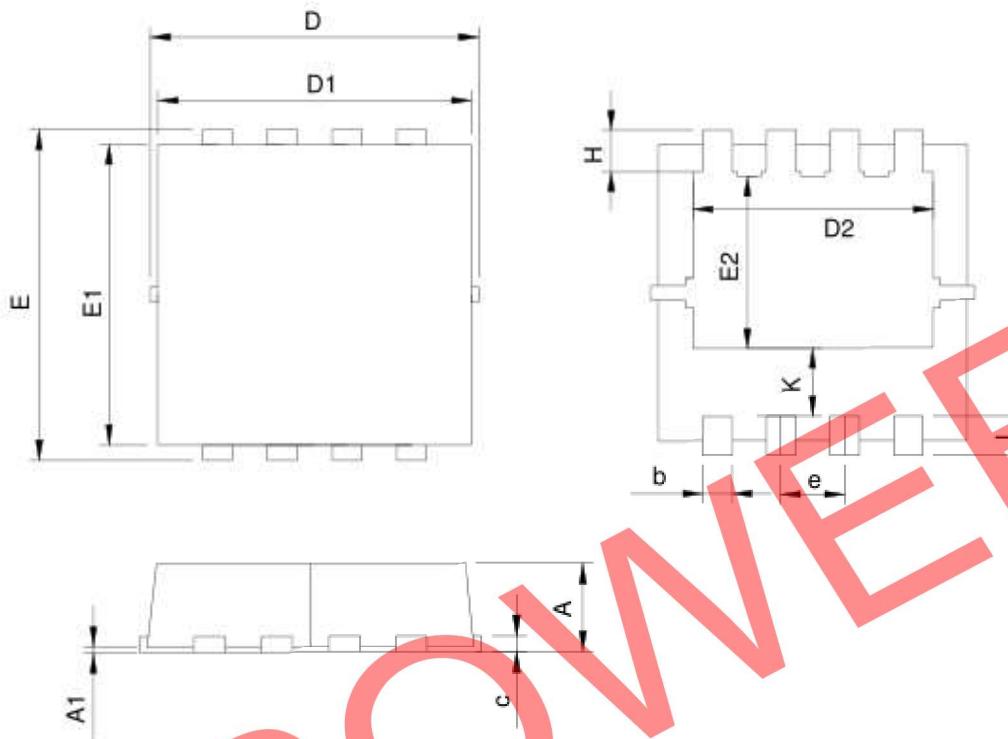
Fig.12 Avalanche Waveform



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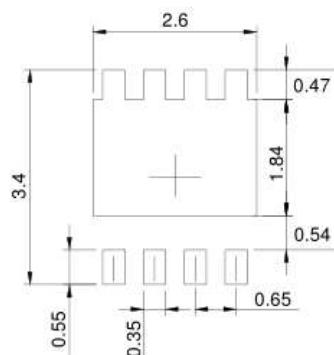
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- Dimensions(DFN3x3)



| SYMBOL | DFN3.3x3.3-8 | | | |
|--------|--------------|------|-----------|-------|
| | MILLIMETERS | | INCHES | |
| | MIN. | MAX. | MIN. | MAX. |
| A | 0.70 | 1.00 | 0.028 | 0.039 |
| A1 | 0.00 | 0.05 | 0.000 | 0.002 |
| b | 0.25 | 0.35 | 0.010 | 0.014 |
| c | 0.14 | 0.20 | 0.006 | 0.008 |
| D | 3.10 | 3.50 | 0.122 | 0.138 |
| D1 | 3.05 | 3.25 | 0.120 | 0.128 |
| D2 | 2.35 | 2.55 | 0.093 | 0.100 |
| E | 3.10 | 3.50 | 0.122 | 0.138 |
| E1 | 2.90 | 3.10 | 0.114 | 0.122 |
| E2 | 1.64 | 1.84 | 0.065 | 0.072 |
| e | 0.65 BSC | | 0.026 BSC | |
| H | 0.32 | 0.52 | 0.013 | 0.020 |
| K | 0.59 | 0.79 | 0.023 | 0.031 |
| L | 0.25 | 0.55 | 0.010 | 0.022 |

RECOMMENDED LAND PATTERN



UNIT: mm