

## Features

- Uses CRM(CQ) advanced SkyMOS1 technology
- Extremely low on-resistance  $R_{DS(on)}$
- Excellent  $Q_g \times R_{DS(on)}$  product(FOM)
- Qualified according to JEDEC criteria

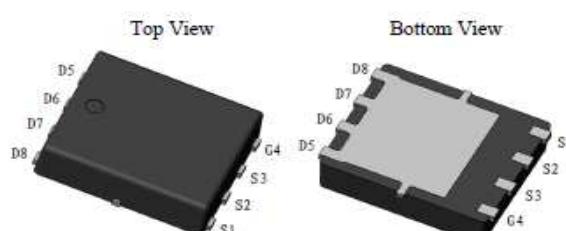
## Product Summary

$V_{DS}$	85V
$R_{DS(on)}$	4.7mΩ
$I_D$	60A

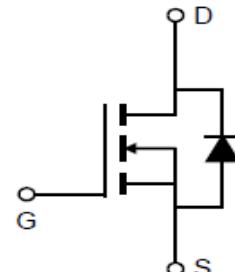
## Applications

- Motor control and drive
- Battery management
- UPS (Uninterruptible Power Supplies)

**100% Avalanche Tested**



CRSM053N08N



## Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRSM053N08N	-	DFN5X6	Tape&Reel	N/A	N/A	5000pcs

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	85	V
Continuous drain current $T_C = 25^\circ\text{C}$ (Silicon limit) $T_C = 25^\circ\text{C}$ (Package limit) $T_C = 100^\circ\text{C}$ (Silicon limit)	$I_D$	90 60 57	A
Pulsed drain current ( $T_C = 25^\circ\text{C}$ , $t_p$ limited by $T_{jmax}$ )	$I_{D\text{ pulse}}$	240	A
Avalanche energy, single pulse ( $L=0.3\text{mH}$ , $R_g=25\Omega$ ) <sup>[1]</sup>	$E_{AS}$	126	mJ
Gate-Source voltage	$V_{GS}$	$\pm 20$	V
Power dissipation ( $T_C = 25^\circ\text{C}$ )	$P_{tot}$	79	W
Operating junction and storage temperature	$T_j$ , $T_{stg}$	-55...+150	°C

Notes: 1. EAS was tested at  $T_j = 25^\circ\text{C}$ ,  $I_D = 29\text{A}$ .

**Thermal Resistance**

Parameter	Symbol	Max	Unit
Thermal resistance, junction - case.	R <sub>thJC</sub>	1.58	°C/W
Thermal resistance, junction - ambient(min. footprint)	R <sub>thJA</sub>	47	

**Electrical Characteristic (at T<sub>j</sub> = 25 °C, unless otherwise specified)**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

**Static Characteristic**

Drain-source breakdown voltage	BV <sub>DSS</sub>	85	97	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
Gate threshold voltage	V <sub>GS(th)</sub>	2	3	4	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
Zero gate voltage drain current	I <sub>DSS</sub>	-	0.05	1	μA	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V T <sub>j</sub> =25°C T <sub>j</sub> =125°C
Gate-source leakage current	I <sub>GSS</sub>	-	10	100	nA	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V
Drain-source on-state resistance	R <sub>DS(on)</sub>	-	4.7	5.6	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =30A
Transconductance	g <sub>fs</sub>	-	84.2	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =50A

**Dynamic Characteristic**

Input Capacitance	C <sub>iss</sub>	-	3086	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =40V, f=1MHz
Output Capacitance	C <sub>oss</sub>	-	1057	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	26	-		
Gate Total Charge	Q <sub>G</sub>	-	55	-	nC	V <sub>GS</sub> =10V, V <sub>DS</sub> =40V, I <sub>D</sub> =50A, f=1MHz
Gate-Source charge	Q <sub>gs</sub>	-	15	-		
Gate-Drain charge	Q <sub>gd</sub>	-	13	-		
Turn-on delay time	t <sub>d(on)</sub>	-	20.1	-		
Rise time	t <sub>r</sub>	-	38.9	-		
Turn-off delay time	t <sub>d(off)</sub>	-	45.1	-	ns	V <sub>GS</sub> =10V, V <sub>DD</sub> =40V R <sub>G_ext</sub> =3.0Ω
Fall time	t <sub>f</sub>	-	22.8	-		
Gate resistance	R <sub>G</sub>	-	3.3	-	Ω	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz

**Body Diode Characteristic**

<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>			<b>Unit</b>	<b>Test Condition</b>
		<b>min.</b>	<b>typ.</b>	<b>max.</b>		
Body Diode Forward Voltage	V <sub>SD</sub>	-	0.95	1.4	V	V <sub>GS</sub> =0V, I <sub>SD</sub> =50A
Body Diode Reverse Recovery Time	t <sub>rr</sub>	-	54	-	ns	I <sub>F</sub> =30A, dI/dt=300A/us;
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	-	250	-	nC	

## Typical Performance Characteristics

Fig 1: Output Characteristics

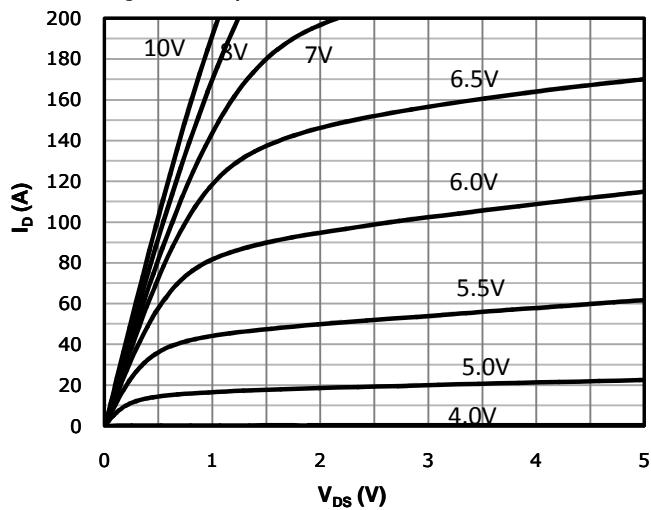


Fig 2: Transfer Characteristics

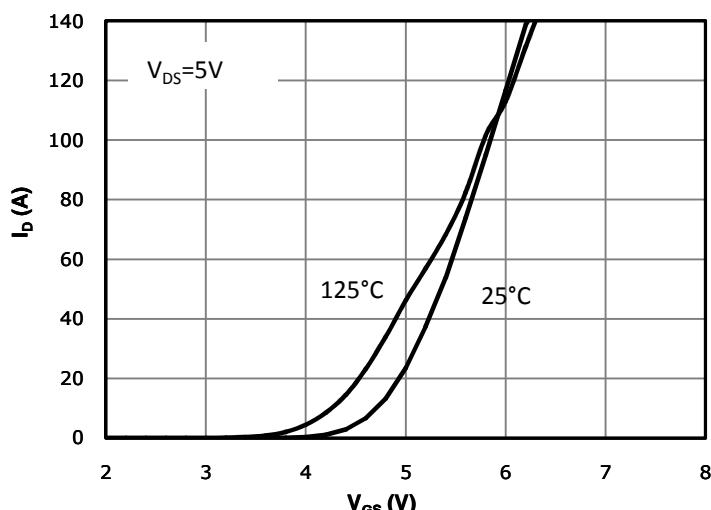


Fig 3:  $R_{DS(on)}$  vs Drain Current and Gate Voltage

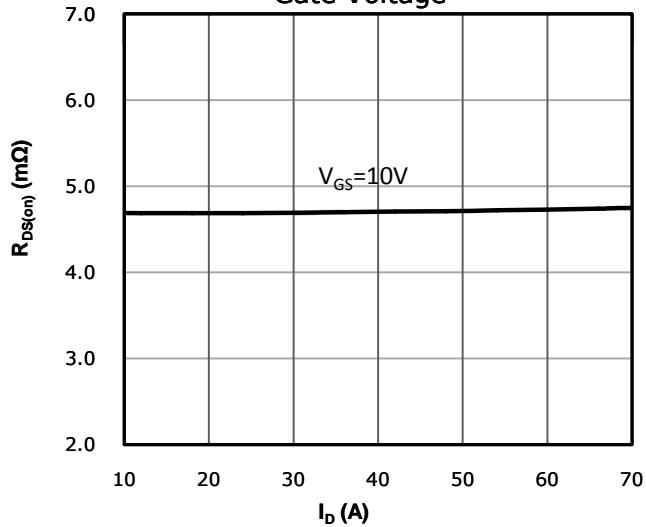


Fig 4:  $R_{DS(on)}$  vs Gate Voltage

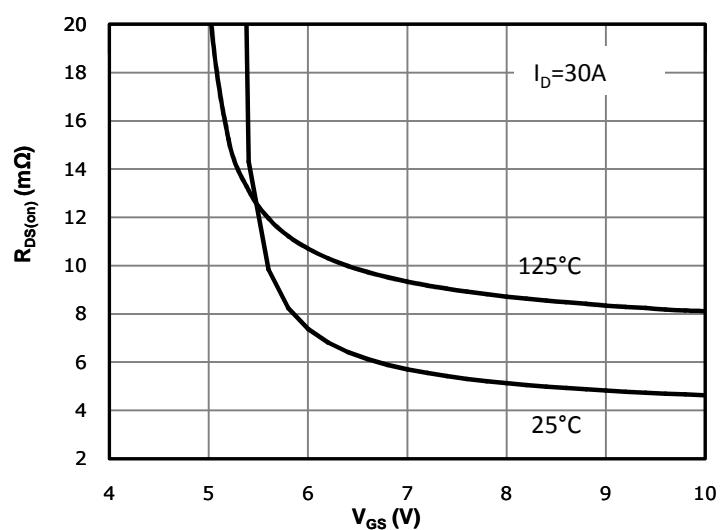


Fig 5:  $R_{DS(on)}$  vs. Temperature

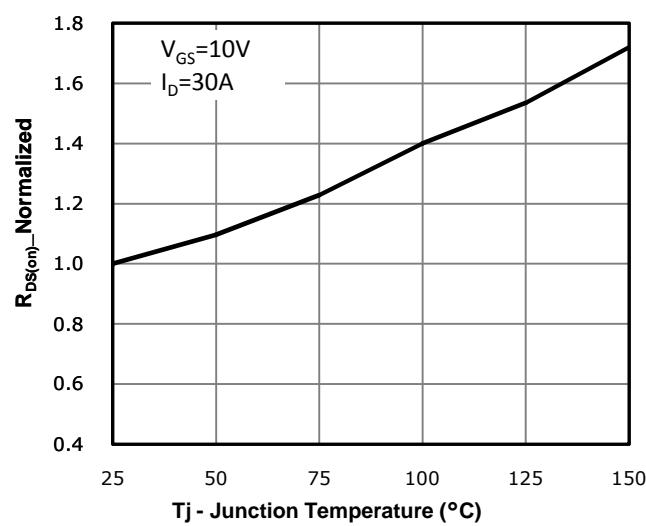


Fig 6: Capacitance Characteristics

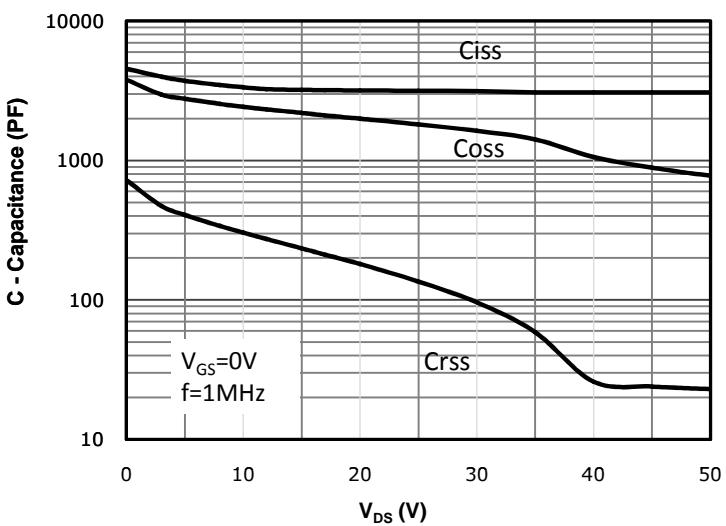


Fig 7: Gate Charge Characteristics

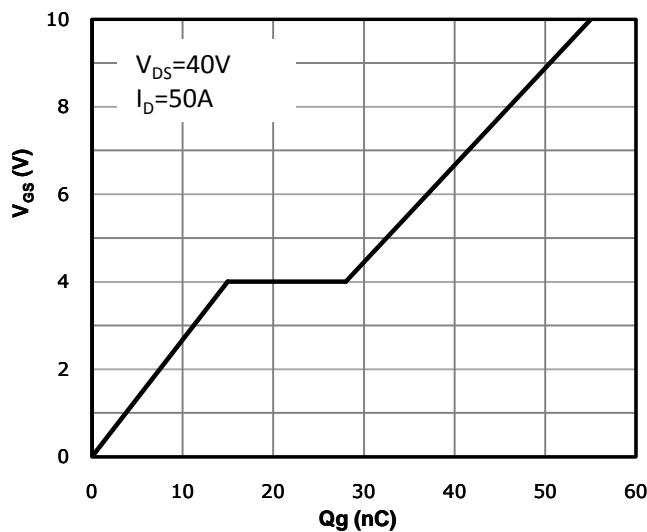


Fig 8: Body-diode Forward Characteristics

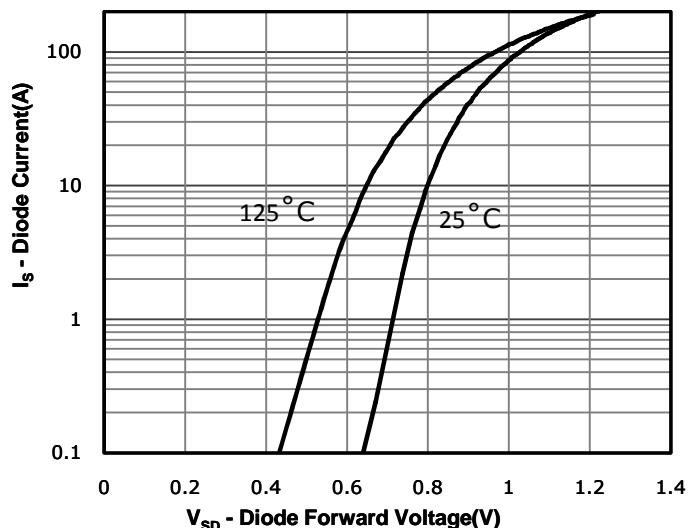


Fig 9: Power Dissipation

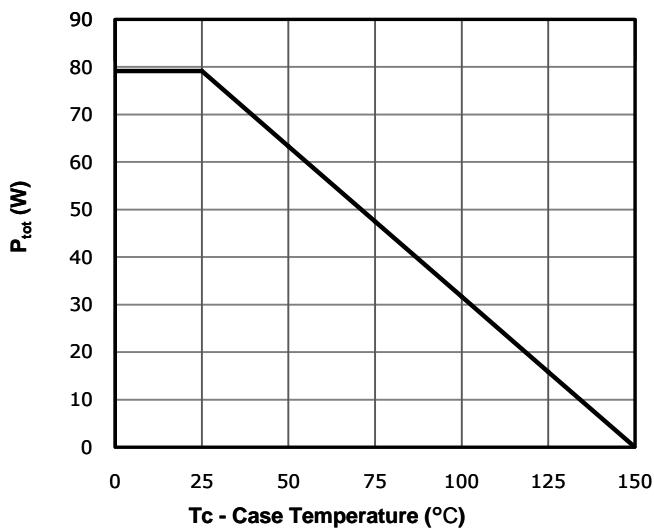


Fig 10: Drain Current Derating

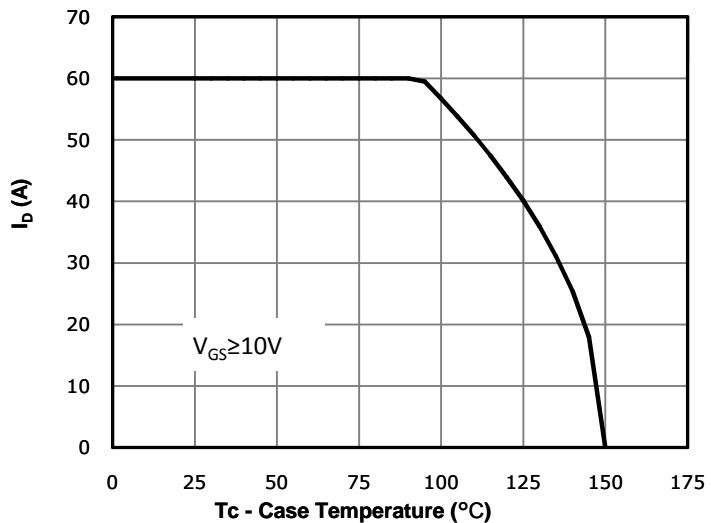


Fig 11: Safe Operating Area

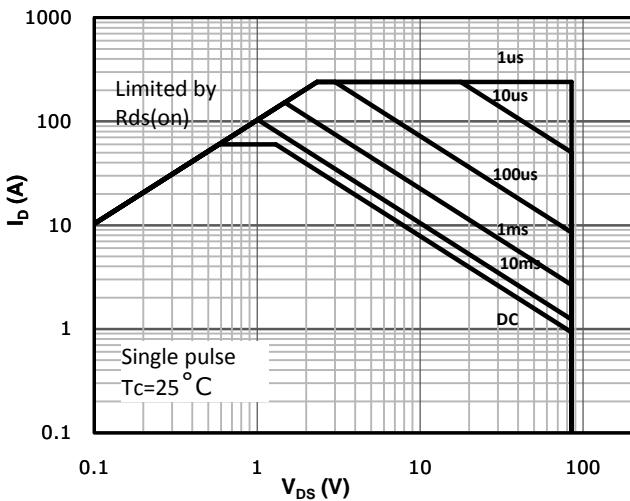
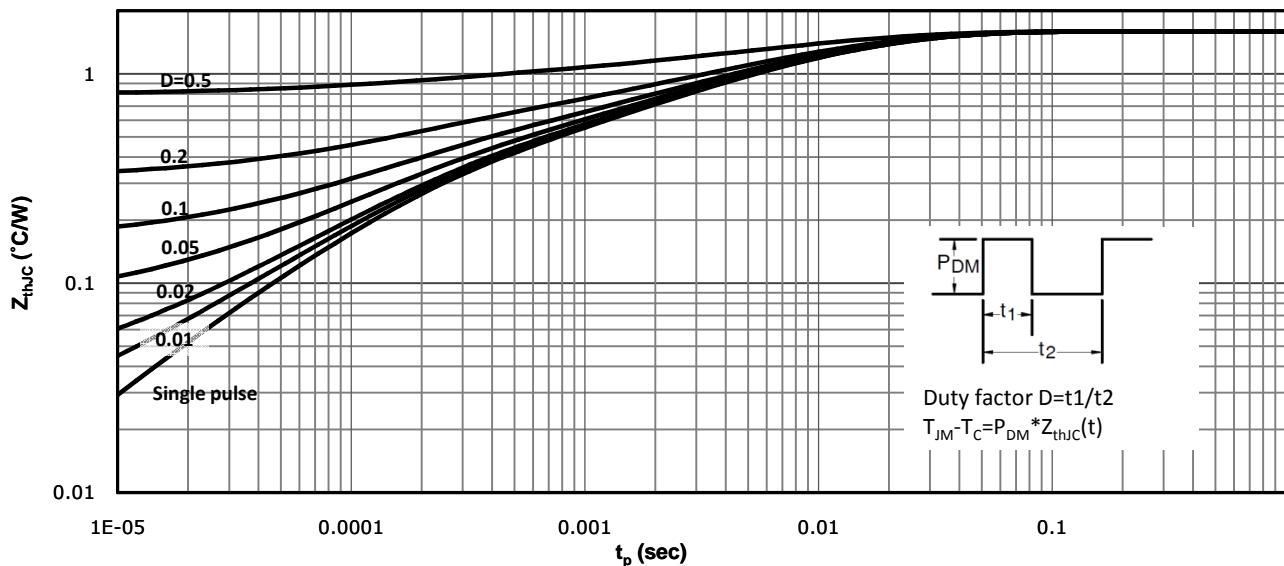
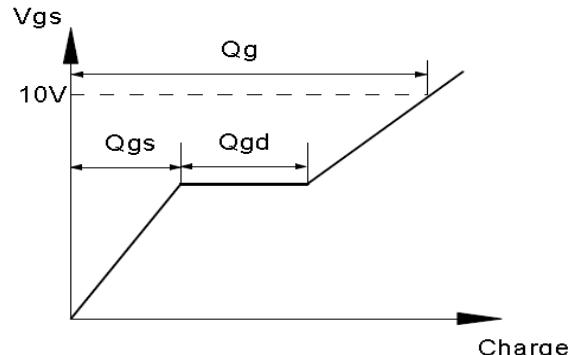
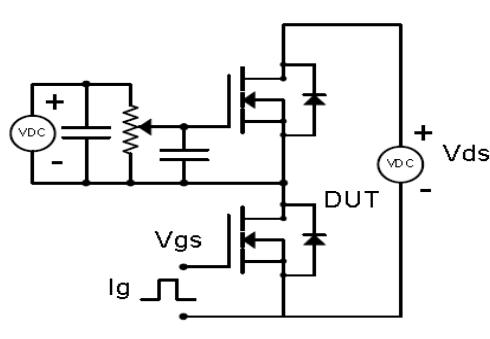


Fig 12: Max. Transient Thermal Impedance

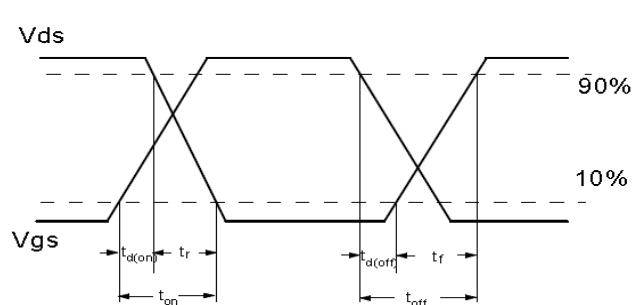
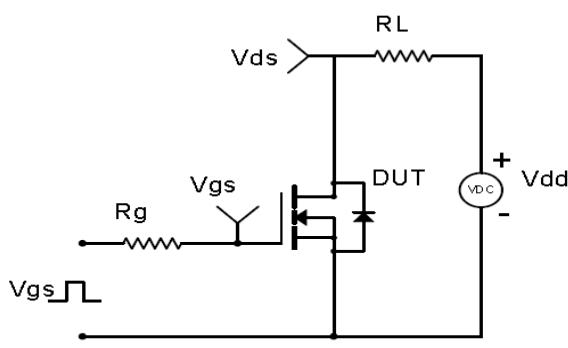


**Test Circuit & Waveform**

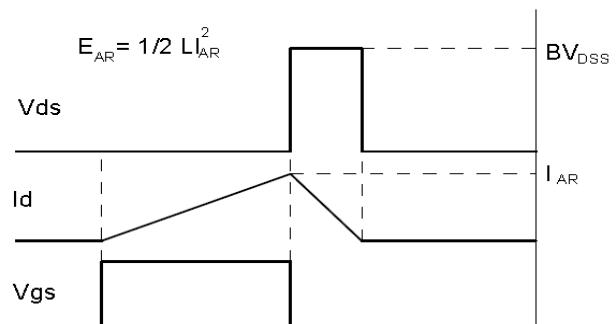
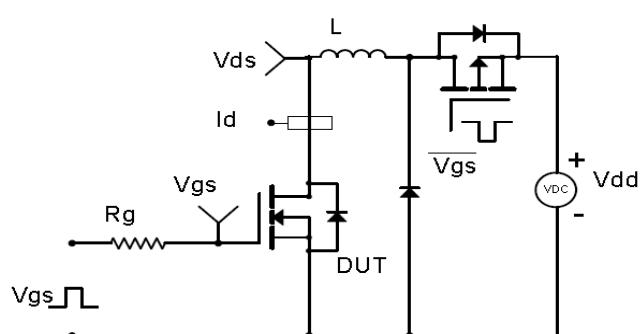
Gate Charge Test Circuit &amp; Waveform



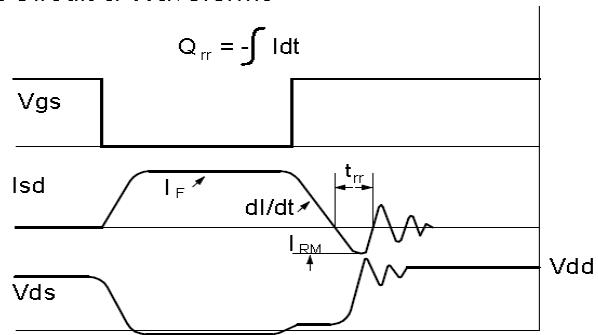
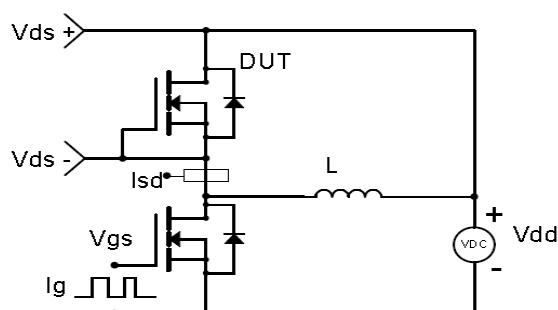
Resistive Switching Test Circuit &amp; Waveforms

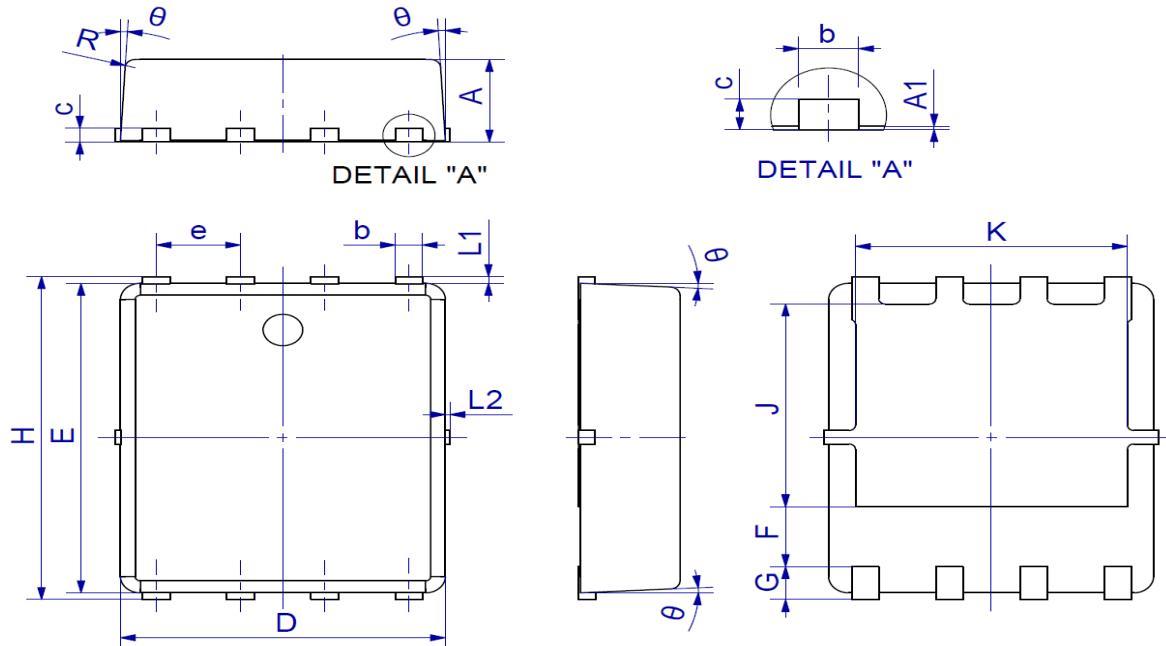


Unclamped Inductive Switching (UIS) Test Circuit &amp; Waveforms



Diode Recovery Test Circuit &amp; Waveforms



**Package Outline: DFN5\*6**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.80	1.00	0.31	0.39
A1	0.00	0.05	0.00	0.02
b	0.35	0.49	0.14	0.19
c	0.254 REF.		0.1REF.	
D	4.90	5.10	1.93	2.01
E	5.70	5.90	2.24	2.32
e	1.27 BSC.		0.5 BSC.	
F	1.40 REF.		0.551 REF.	
G	0.60 REF.		0.236 REF.	
H	5.95	6.20	2.34	2.44
J	3.40 REF.		1.339 REF.	
K	4.00 REF.		1.575 REF.	
L1	0.10	0.15	0.04	0.06
L2	----	0.02	----	0.01
θ	10° TYP.		10° TYP.	



华润微电子(重庆)有限公司

CRSM053N08N

SkyMOS1 N-MOSFET 85V, 4.7mΩ, 60A

## Revision History

Revison	Date	Major changes
1.0	2018-12-03	Release of formal version.

## Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.

