

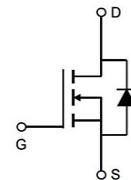
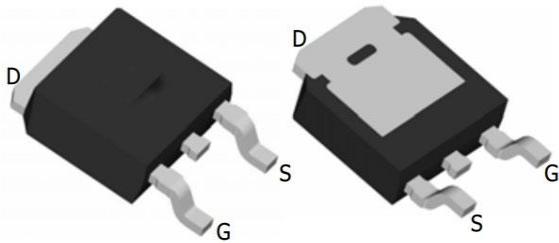
**30V /55A Single N Power MOSFET**
**General Description**

30V /55A Single N Power MOSFET

Very low on-resistance RDS(on) @ VGS=4.5 V

Pb-free lead plating; RoHS compliant

<b>V<sub>DS</sub></b>	30	V
<b>R<sub>DS(on),TYP@VGS=10V</sub></b>	3.9	mΩ
<b>R<sub>DS(on),TYP@VGS=4.5</sub></b>	6.7	mΩ
<b>I<sub>D</sub></b>	55	A



Part ID	Package Type	Marking	Tape and reel infomation
SM4146T9RL	TO-252	55N03	2500


 100% UIS Tested  
100% RG Tested

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	20	±V
Continuous Drain Current A	I <sub>D</sub>	55.0	A
		43*	
Pulsed Drain Current B	I <sub>DM</sub>	88.0	
Avalanche Current G	I <sub>AR</sub>	17.6	
Repetitive avalanche energy L=0.1mH G	E <sub>AR</sub>	40.5	mJ
Power Dissipation A	P <sub>D</sub>	62	W
		31*	
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>TSG</sub>	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A	R <sub>θJA</sub>	23	35	°C/W
Maximum Junction-to-Ambient A		47	57	°C/W
Maximum Junction-to-Lead c	R <sub>θJL</sub>	14	22	°C/W

## STATIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = -250\mu A, V_{GS} = 0V$	30			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$			1	uA
					5	
$I_{GSS}$	Gate-Body leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.5	2.3	3	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=20A$		3.9	5.6	$m\Omega$
		$V_{GS}=4.5V, I_D=20A$		6.7	9.5	
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D=20A$		67		S
$V_{SD}$	Diode Forward Voltage	$I_S=1A, V_{GS}=182V$		0.72	1	V
$I_S$	Maximum Body-Diode Continuous Current				55	A

## DYNAMIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, f=1MHz$		2037	2485	pF
$C_{oss}$	Output Capacitance			375	461	pF
$C_{rss}$	Reverse Transfer Capacitance			220	261	pF
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$			2.3	$\Omega$

## SWITCHING PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$Q_g(10V)$	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V, I_D=20A$		16		nC
$Q_g 4.5V$	Total Gate Charge			8		
$Q_{gs}$	Gate Source Charge			3.22		
$Q_{gd}$	Gate Drain Charge			4.6		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS}=10V, V_{DS}=15V, RL=0.75\Omega, R_{GEN}=3\Omega$		5		ns
$t_r$	Turn-On Rise Time			4		
$t_{D(off)}$	Turn-Off Delay Time			14		
$t_f$	Turn-Off Fall Time			4.5		
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F=-8A, dI/dt=500A/\mu s$		10		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$I_F=18A, dI/dt=500A/\mu s$		15		nC

