

# N-Channel 20 V (D-S) MOSFET

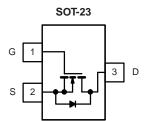
PRODUCT SUMMARY	
V <sub>DS</sub> (V)	20
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5V$	0.020
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 2.5V$	0.025
Q <sub>g</sub> typ. (nC	4.0
I <sub>D</sub> (A) <sup>a, e</sup>	7
Configuration	Single

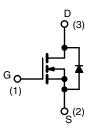
#### **FEATURES**

- TrenchFET® power MOSFET
- Low on-resistance
- 100 % R<sub>g</sub> tested
- Material categorization: for definitions of compliance please see



RoHS





N-Channel MOSFET

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		$V_{DS}$	20	
Gate-source voltage		$V_{GS}$	± 12	V
	T <sub>C</sub> = 25 °C		7 <sup>e</sup>	
Continuous dusin surrent /T 150 °C\	T <sub>C</sub> = 70 °C	1 .	5 <sup>e</sup>	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	l <sub>D</sub>	5.0 b, c	
	T <sub>A</sub> = 70 °C		4.4 b, c	А
Pulsed drain current (t = 300 μs)		I <sub>DM</sub>	26	
	T <sub>C</sub> = 25 °C	Is	2.1	
Continuous source-drain diode current	T <sub>A</sub> = 25 °C		1.1 <sup>b, c</sup>	
	T <sub>C</sub> = 25 °C	T <sub>C</sub> = 25 °C	2.8	
Manian and a sure discipation	T <sub>C</sub> = 70 °C	5	1.6	14/
Maximum power dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.3 <sup>b, c</sup>	W
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Soldering recommendations (peak temperature)			260	→ °C

THERMAL RESISTANCE RATINGS							
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient b, d	t ≤ 5 s	$R_{thJA}$	75	100	°C/W		
Maximum junction-to-foot (drain)	Steady state	Rt <sub>hJF</sub>	40	50	] C/W		

#### Notes

- a. Based on  $T_C = 25~^{\circ}C$
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 166 °C/W
- e. Package limited



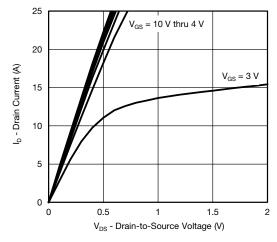
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	$V_{DS}$	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	20	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	I 050 ·· A	-	30	-	m\//°C	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	-	-4.8	-	mV/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	0.5	-	1.5	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zeve cete veltere ducin avvent		$V_{DS} = 20V, V_{GS} = 0 V$	-	-	1		
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	-	-	10	μA	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, V_{GS} = 10 \text{ V}$	20	-	-	Α	
Drain course on state recistance 8	Б	$V_{GS} = 4.5V, I_D = 5.5 A$	-	0.020	-		
Drain-source on-state resistance a	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 5 \text{ A}$	-	0.025	-	Ω	
Forward transconductance a	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5.5 A	-	24	-	S	
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>		-	424	-		
Output capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	100	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>		-	42	-		
Total gate charge	Qg	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.5 A	-	8.2	13		
			-	4.2	7	0	
Gate-source charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5.5 \text{ A}$	-	1.4	-	nC	
Gate-drain charge	Q <sub>gd</sub>		-	1.4	-	1	
Gate resistance	$R_{g}$	f = 1 MHz	2.5	12.6	25.2	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	6	12		
Rise time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, R_L = 3.4 \Omega$	-	20	30		
Turn-off delay time	t <sub>d(off)</sub>	$I_D\cong 4.4$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$	-	14	21		
Fall time	t <sub>f</sub>		-	10	20		
Turn-on delay time	t <sub>d(on)</sub>		-	3	6	ns	
Rise time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 3.4 $\Omega$	-	11	20	- -	
Turn-off delay time	t <sub>d(off)</sub>	$I_D\cong 4.4$ A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$	-	20	30		
Fall time	t <sub>f</sub>		-	7	14		
<b>Drain-Source Body Diode Characterist</b>	cs						
Continuous source-drain diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	7	-	_	
Pulse diode forward current	I <sub>SM</sub>		-	-	25	A	
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 4.4 A, V <sub>GS</sub> = 0 V	-	0.82	1.2	V	
Body diode reverse recovery time	t <sub>rr</sub>		-	13	20	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	$I_F = 4.4 \text{ A}, \text{ di/dt} = 100 \text{ A/µs},$	_	6	12	nC	
Reverse recovery fall time	ta	T <sub>J</sub> = 25 °C	-	8	-	1	
Reverse recovery rise Time	t <sub>b</sub>		_	5	-	ns	

## Notes

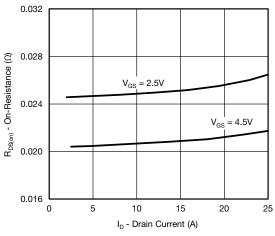
- a. Pulse test; pulse width  $\leq 300~\mu s,\,duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

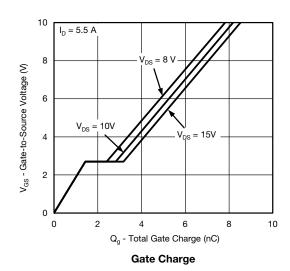


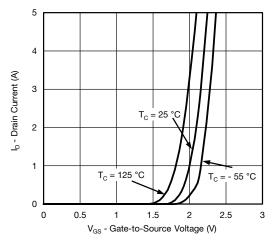


#### **Output Characteristics**

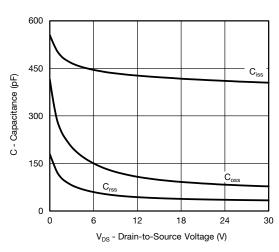


On-Resistance vs. Drain Current and Gate Voltage

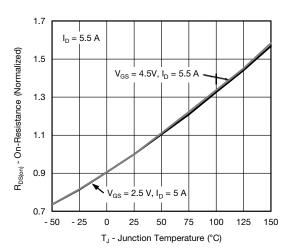




**Transfer Characteristics** 

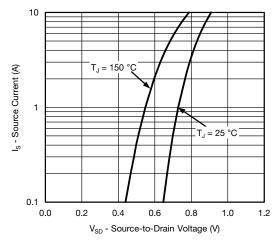


Capacitance

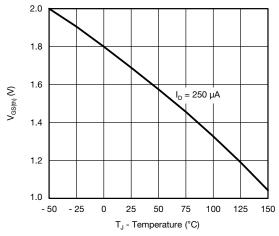


On-Resistance vs. Junction Temperature

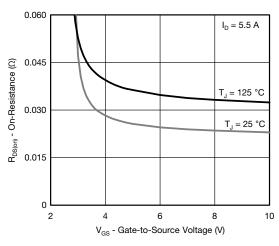




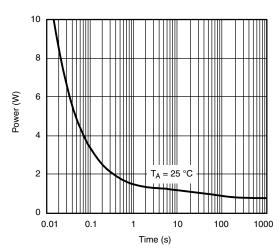
#### Source-Drain Diode Forward Voltage



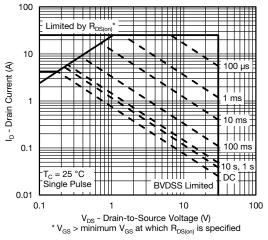
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage

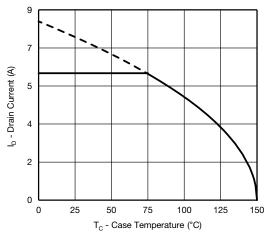


Single Pulse Power (Junction-to-Ambient)

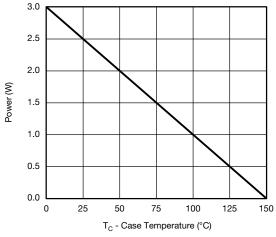


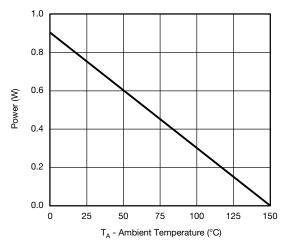
Safe Operating Area, Junction-to-Ambient





#### Current Derating a





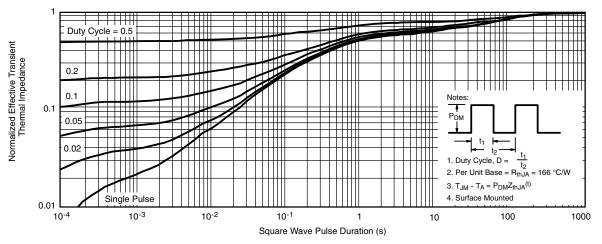
Power Derating, Junction-to-Foot

Power Derating, Junction-to-Ambient

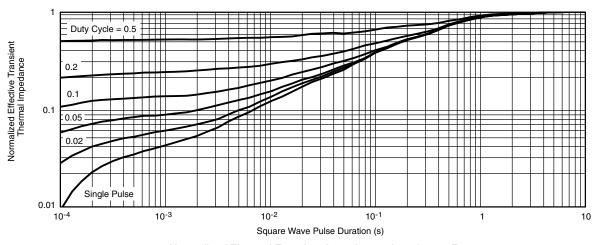
#### Note

a. The power dissipation  $P_D$  is based on  $T_U$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





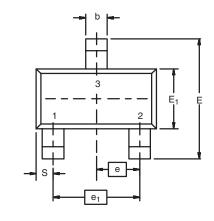
Normalized Thermal Transient Impedance, Junction-to-Ambient

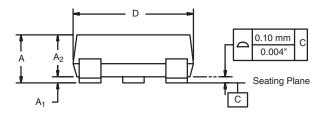


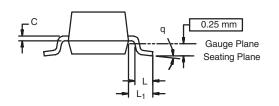
Normalized Thermal Transient Impedance, Junction-to-Foot



## SOT-23 (TO-236): 3-LEAD





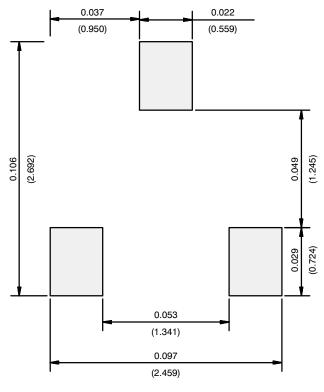


Dim -	MILLIM	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.074	8 Ref	
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025	5 Ref	
S	0.50 Ref		0.020	) Ref	
q	3°	8°	3°	8°	

ECN: S-03946-Rev. K, 09-Jul-01 DWG: 5479



## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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