

# **30V P-Channel Trench MOSFET**



Absolute Maximum Ratir	ngs T	C <sub>c</sub> = 25⁰C, unless oth	erwise noted		
Parameter			Symbol	Value	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	-30	V	
Continuous Drain Current		T <sub>C</sub> = 25°C	· I <sub>D</sub>	-90	A
		T <sub>C</sub> = 100°C		-63	
Pulsed Drain Current (note1)			I <sub>DM</sub>	-360	A
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
Single Pulse Avalanche Energy (note2)		E <sub>AS</sub>	135	mJ	
Avalanche Current			I <sub>As</sub>	-30	А
Power Dissipation (note3	(n a t a 2)	T <sub>C</sub> = 25°C	P	79	W
	note3)	$T_{\rm C} = 25^{\rm o}{\rm C}$ $T_{\rm C} = 100^{\rm o}{\rm C}$	P <sub>D</sub>	39.5	W
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55~+175	°C

Thermal Resistance					
Parameter	Symbol	Value	Unit		
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.9	00000		
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	60	°C/W		



<b>Specifications</b> $T_J = 25^{\circ}C$ , ur	less othe	rwise noted				
Deremeter	Symbol	Test Conditions	Value			m   t
Parameter			Min.	Тур.	Max.	Unit
Static		•				
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_{D} = -250\mu A$	-30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -30V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			-1	μA
		V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 100°C			-25	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1.0	-1.7	-2.4	V
Durain Courses On Descintances (Note2)	D	V <sub>GS</sub> = -10V, I <sub>D</sub> = -20A		6.3	7.5	mΩ
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -20A		10	12	mΩ
Forward Transconductance (Note3)	<b>g</b> <sub>fs</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -20A	30			S
Dynamic		•				
Input Capacitance	C <sub>iss</sub>	$\gamma = 0 \gamma$		4942		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = -15V,$		473		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		461		
Total Gate Charge	Q <sub>g</sub>			82		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DD</sub> = -15V, I <sub>D</sub> = -20A, V <sub>GS</sub> = -10V		14		
Gate-Drain Charge	$Q_{gd}$			16		
Turn-on Delay Time	t <sub>d(on)</sub>			182		
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> = -15V, I <sub>D</sub> = -20A,		262		ns
Turn-off Delay Time	t <sub>d(off)</sub>	$V_{\text{DD}} = -15 \text{V}, \text{ I}_{\text{D}} = -20 \text{A}, \\ \text{R}_{\text{G}} = 2.5 \Omega$		1.3		
Turn-off Fall Time	t <sub>f</sub>			9.8		
Drain-Source Body Diode Characteri	stics					
Continuous Body Diode Current	I <sub>s</sub>	T 0700			-90	A
Pulsed Diode Forward Current	I <sub>SM</sub>	$T_{\rm C} = 25^{\circ}{\rm C}$			-360	
Body Diode Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25°C, I <sub>SD</sub> = -15A, V <sub>GS</sub> = 0V			-1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -15A,		34		ns
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt = 100A/µs		79		nC

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = -30A, L=0.3mH,  $V_{DD}$  = 30V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse width  $\leq$  300µs, Duty Cycle  $\leq$  1%



### **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted











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Figure B: Resistive Switching Test Circuit and Waveform



Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-252



Unit: mm				
Symbol	Min.	Max.		
A	2.20	2.40		
A1	0.00	0.20		
A2	0.97	1.17		
b	0.68	0.90		
b3	5.20	5.50		
с	0.43	0.63		
D	5.98	6. 22		
D1	5. 30REF			
E	6.40	6.80		
E1	4.63	_		

Unit: mm				
Symbol	Min.	Max.		
e	2. 286BSC			
Н	9.40	10.50		
L	1.38	1.75		
L1	2.90REF			
L2	0. 51BSC			
L3	0.88	1.28		
L4	-	1.00		
L5	1.65	1.95		
θ	0°	8°		



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