

TSA50N20MK 200V N-Channel MOSFET

General Description

This Power MOSFET is produced using Truesemi's advanced planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.



Features

- 50A,200V,Max. $R_{DS(on)}$ =0.038 $\Omega @ V_{GS}$ =10V
- Low gate charge
- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Absolute Maximum Ratings

 $T_J=25^{\circ}C$ unless otherwise specified

Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted					
Parameter	Symbol	Value	Unit		
Drain-Source Voltage (note1)	V _{DSS}	200	V		
Continuous Drain Current	I _D	50	•		
Pulsed Drain Current (note2)	I _{DM}	200	A		
Gate-Source Voltage	V _{GSS}	±20	V		
Single Pulse Avalanche Energy (note2)	E _{AS}	400	mJ		
Power Dissipation (T _C = 25°C)	P _D	34	W/⁰C		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150			

Thermal Resistance Characteristics

Thermal Resistance

Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	0.5	00.00	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	45	°C/W	

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Electrical Characteristics $T_J=25^{\circ}C$ unless otherwise specified

Specifications $T_J = 25^{\circ}C$, unless otherwise noted

Parameter	Symbol		Value			
		Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	$V_{GS} = 0V, I_{D} = 250\mu A$	200			V
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 200V, V _{GS} = 0V, T _J = 25°C			1	μA
Gate-Source Leakage	IGSS	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate-Source Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V
Drain-Source On-Resistance (Note4)	R _{DS(on)}	V _{GS} = 10V, I _D = 25A		0.030	0.038	Ω
Dynamic		1		•	·	
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1.0MHz		3538		pF
Output Capacitance	C _{OSS}			657		
Reverse Transfer Capacitance	C _{rss}			280		
Total Gate Charge	Qg	V _{DD} = 160V, I _D = 50A, V _{GS} 0 to 10V		244		nC
Gate-Source Charge	Q _{gs}			16		
Gate-Drain Charge	Q _{gd}			144		
Turn-on Delay Time	^t d(on)	V _{DD} = 100V, I _D = 50A, VGS =10V.RG = 25Ω		53		
Turn-on Rise Time	tr			65		20
Turn-off Delay Time	^t d(off)			689		ns
Turn-off Fall Time	tf			230		
Drain-Source Body Diode Characte	ristics					
Continuous Source Current	ISD	Integral PN-diode in MOSFET			50	^
Pulsed Source Current	ISM				200	A
Body Forward Voltage	V _{SD}	$I_{S} = 20A, V_{GS} = 0V$			1.5	V
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _F = 10A,		208		ns
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		2.04		μC

NOTES:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10mH, V_{DD} = 50V, R_G = 25 \Omega, Starting T_J = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%

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Figure 3. Drain to Source Resistance vs. Drain Current Figure 4. Drain to Source Resistance vs. Gate to Source Voltage









Figure 6. Body Diode Forward Characteristics



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