



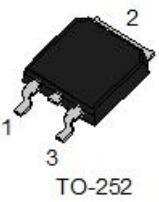
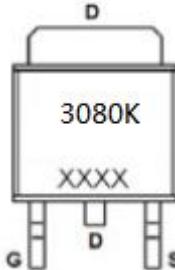
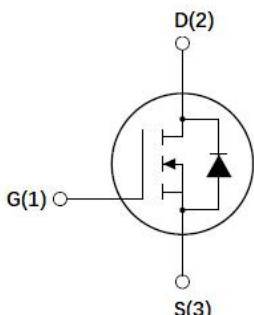
深圳市富满电子集团股份有限公司

SHEN ZHEN FINE MADE ELECTRONICS GROUP CO., LTD.

3080K (文件编号: S&CIC1775)

N-Channel Trench Power MOSFET

Description

Features <ul style="list-style-type: none">• 30V,80A• $R_{DS(ON)}=3.8m\Omega$ (Typ.) @ $V_{GS}=10V$• $R_{DS(ON)}=6.5m\Omega$ (Typ.) @ $V_{GS}=4.5V$• Advanced Trench Technology• Provide Excellent $R_{DS(ON)}$ and Low Gate Charge	Application <ul style="list-style-type: none">• Load Switch• PWM Application
Package  TO-252	 

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_c = 25^\circ C$	80	A
		$T_c = 100^\circ C$	62	A
I_{DM}	Pulsed Drain Current ^{note1}		300	A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}		210	mJ
P_D	Power Dissipation	$T_c = 25^\circ C$	81	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		1.87	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ C$



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Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V,$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10V, I_D=30A$	-	3.8	6.5	$\text{m}\Omega$
		$V_{GS}=4.5V, I_D=20A$	-	6.5	10	
g_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=15A$	-	26	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0\text{MHz}$	-	2153	-	pF
C_{oss}	Output Capacitance		-	327	-	pF
C_{rss}	Reverse Transfer Capacitance		-	287	-	pF
Q_g	Total Gate Charge	$V_{DS}=25V, I_D=30A, V_{GS}=10V$	-	45	-	nC
Q_{gs}	Gate-Source Charge		-	3	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	15	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15V, I_D=30A, R_{GEN}=3\Omega, V_{GS}=10V$	-	21	-	ns
t_r	Turn-on Rise Time		-	32	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	59	-	ns
t_f	Turn-off Fall Time		-	34	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current		-	-	80	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	300	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_s=30A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	$I_F=20A, dI/dt=100A/\mu\text{s}$	-	15	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	4	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: $T_J=25^\circ\text{C}, VDD=30V, VG=10V, RG=25\Omega, L=0.5\text{mH}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$



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Typical Performance Characteristics

Figure 1: Output Characteristics

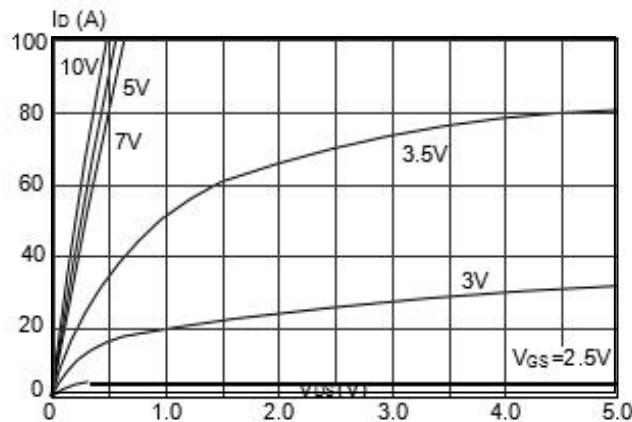


Figure 3: On-resistance vs. Drain Current

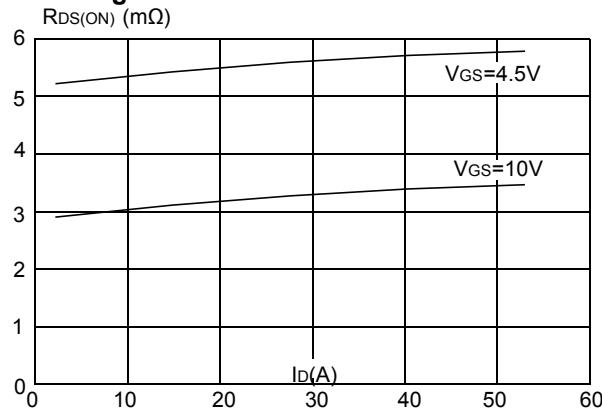


Figure 5: Gate Charge Characteristics

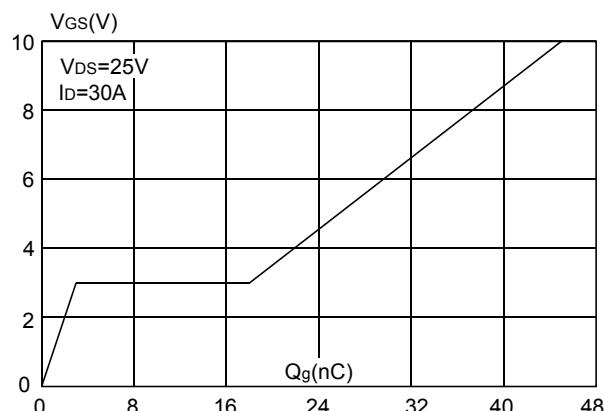


Figure 2: Typical Transfer Characteristics

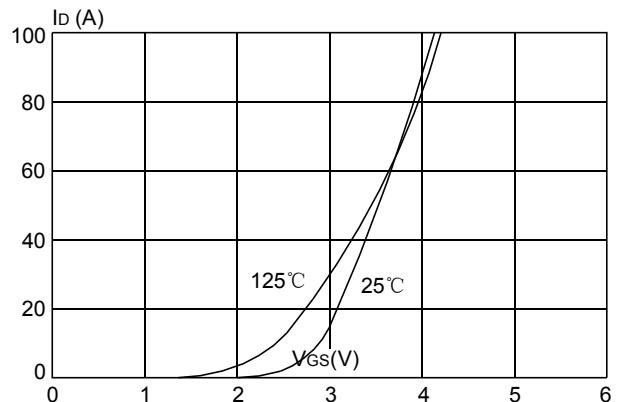


Figure 4: Body Diode Characteristics

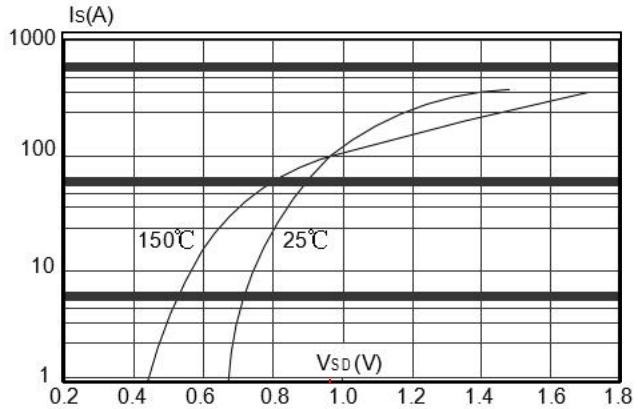
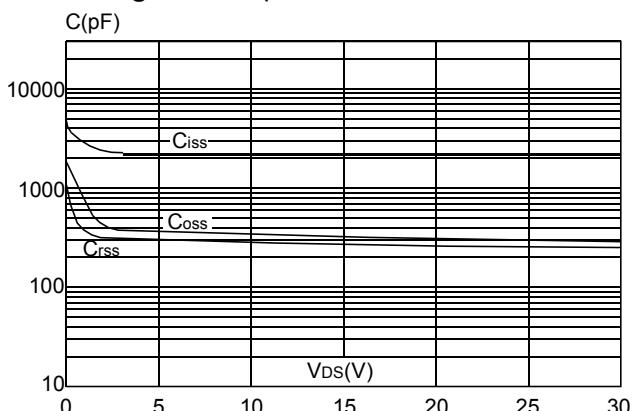


Figure 6: Capacitance Characteristics





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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

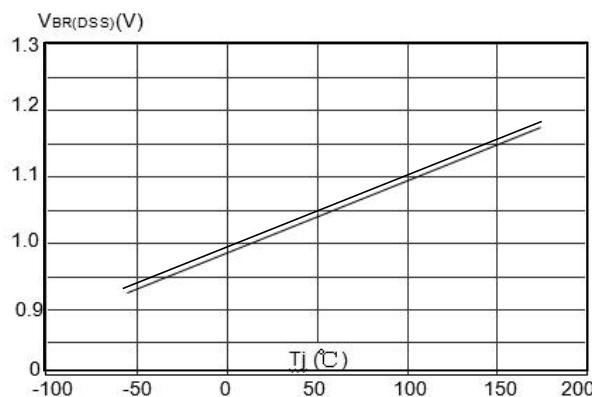


Figure 9: Maximum Safe Operating Area

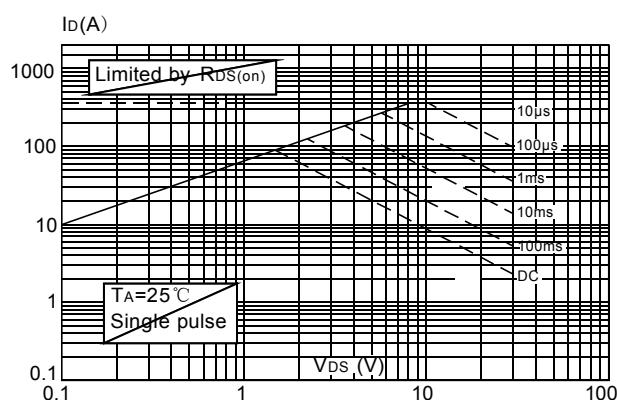


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case (TO-252)

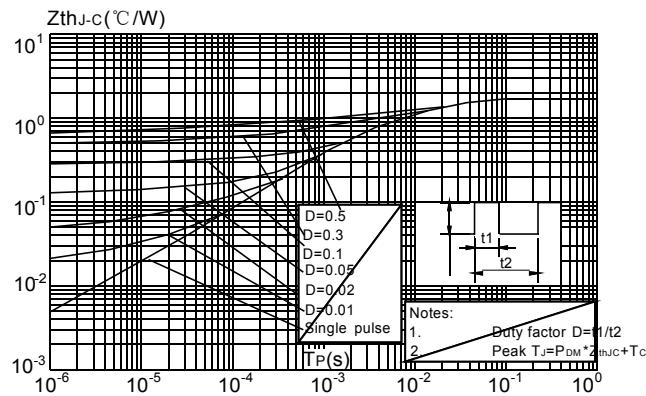


Figure 8: Normalized on Resistance vs. Junction Temperature

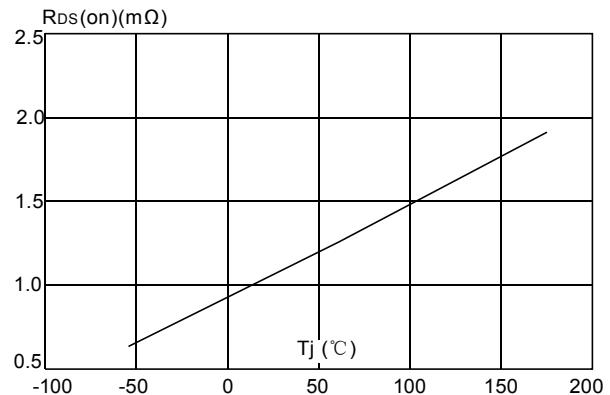
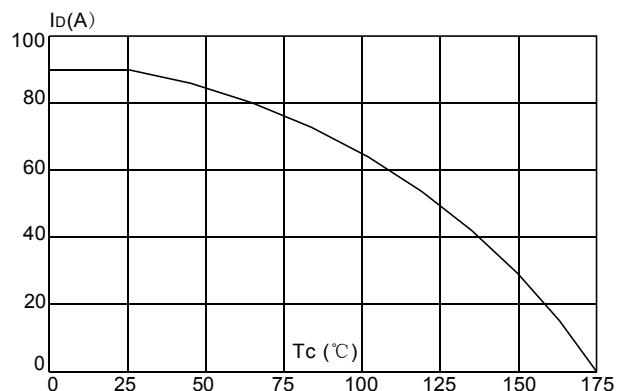


Figure 10: Maximum Continuous Drain Current vs. Case Temperature





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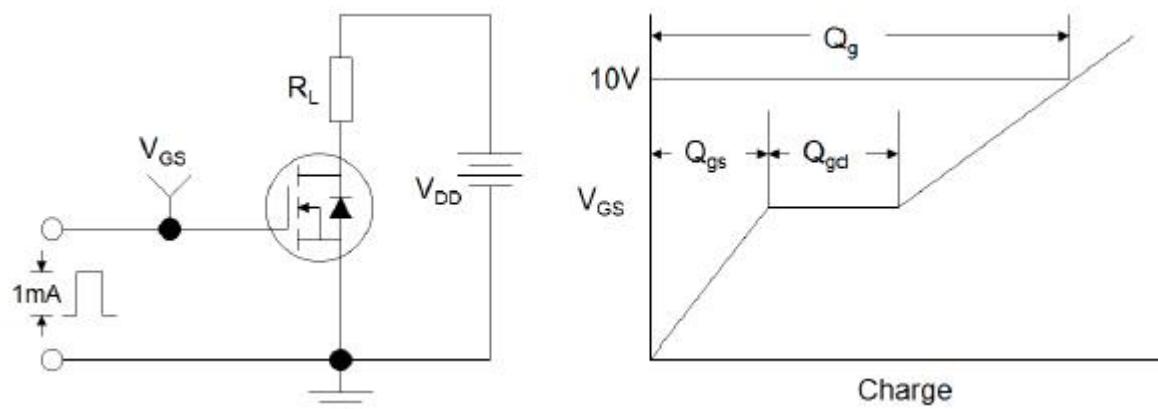


Figure1:Gate Charge Test Circuit & Waveform

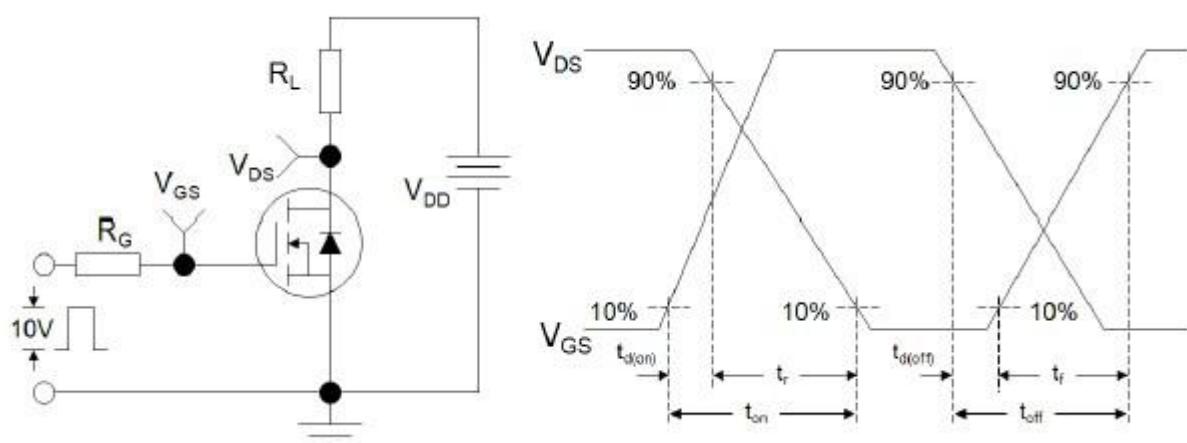


Figure 2: Resistive Switching Test Circuit & Waveforms

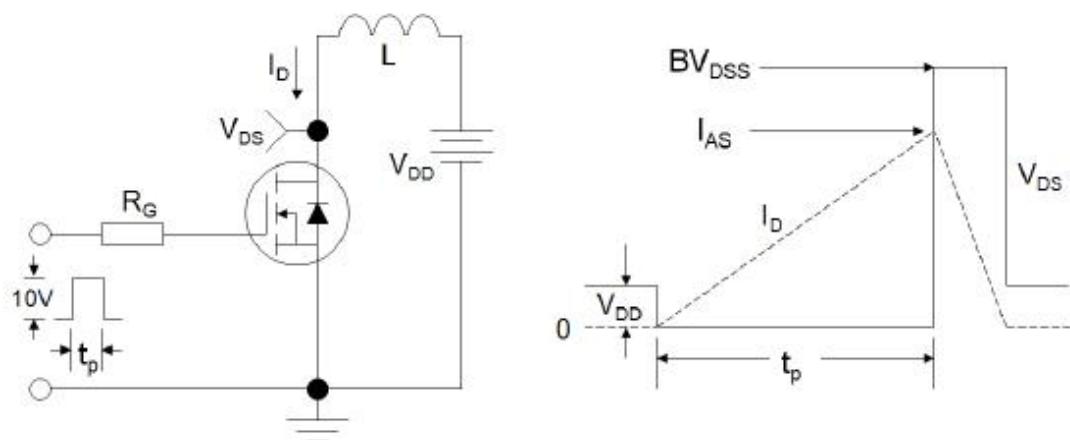


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



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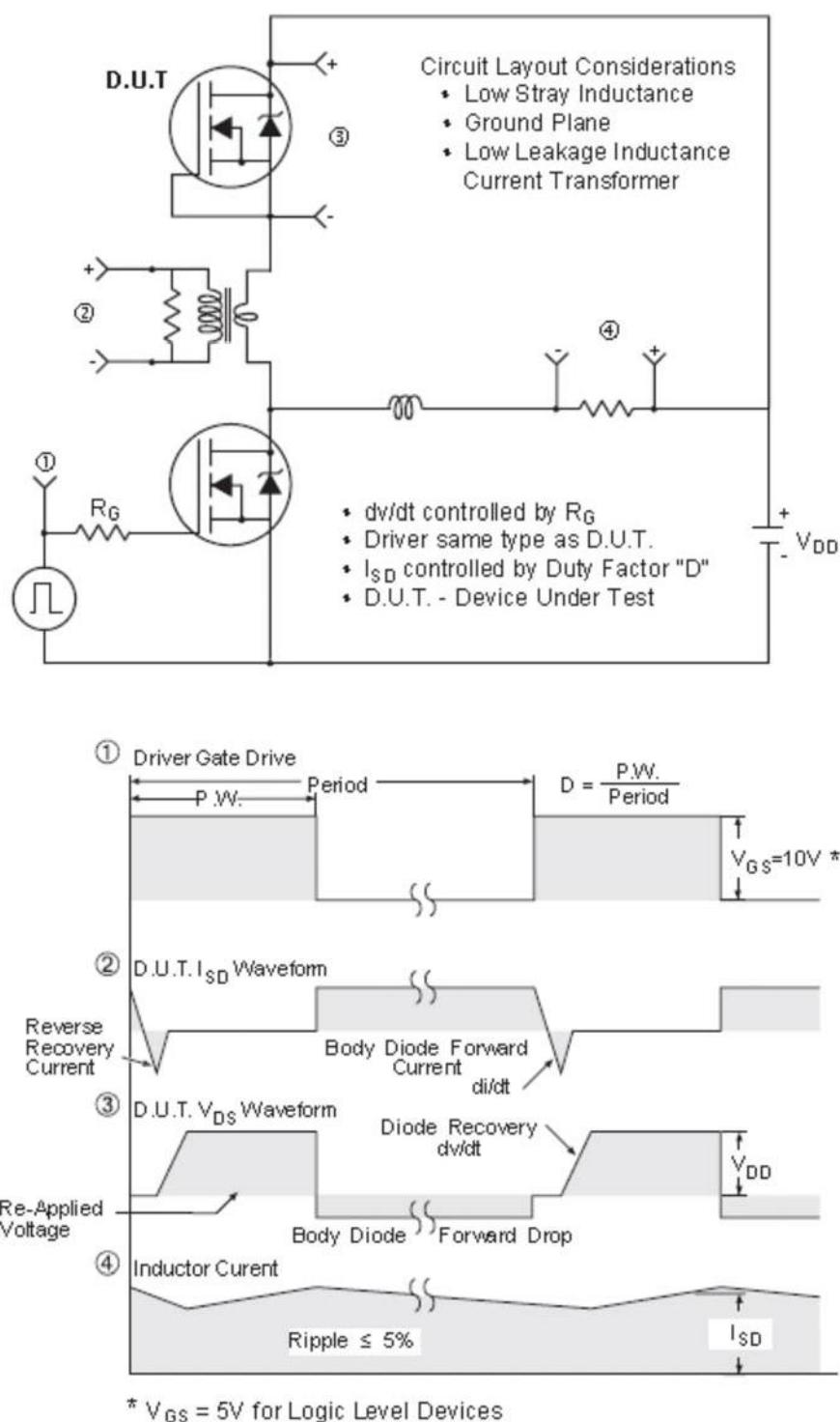


Figure 4:Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)

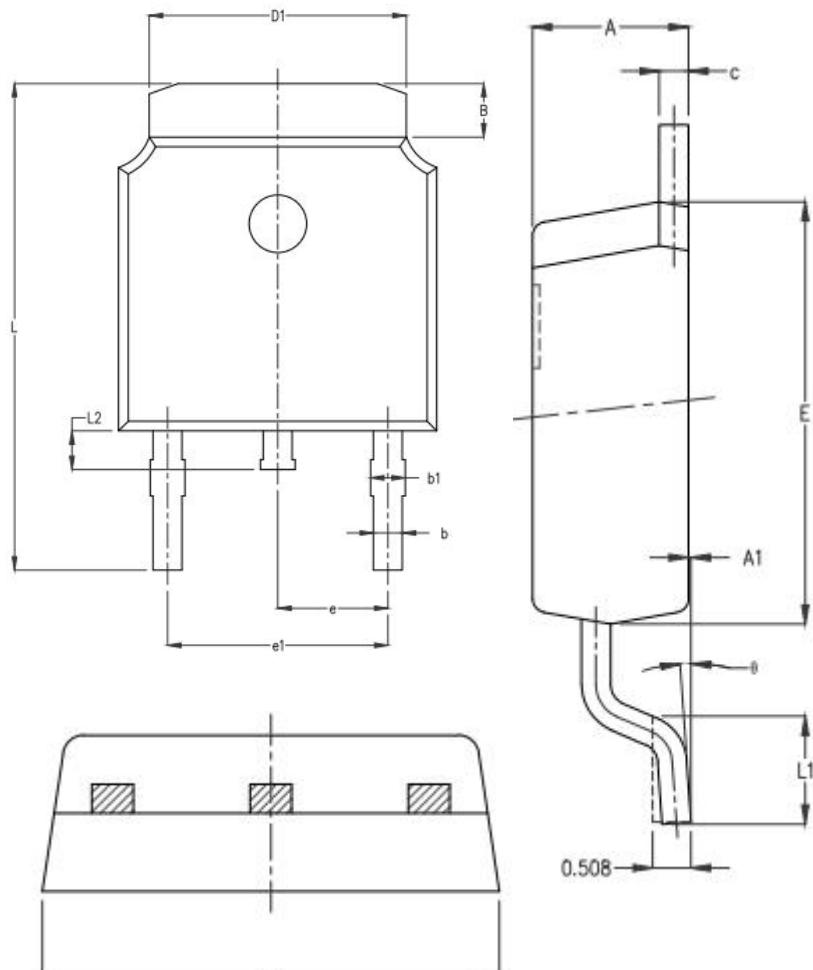


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TO-252 Package Information



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	2.15	2.25	2.35
A1	0.00	0.06	0.12
B	0.96	1.11	1.26
b	0.59	0.69	0.79
b1	0.69	0.81	0.93
c	0.34	0.42	0.50
D	6.45	6.60	6.75
D1	5.23	5.33	5.43
E	5.95	6.10	6.25
e	2.286TYP.		
e1	4.47	4.57	4.67
L	9.90	10.10	10.30
L1	1.40	1.55	1.70
L2	0.60	0.80	1.00
θ	0°	4°	8°