

Description

The HSS3N10 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

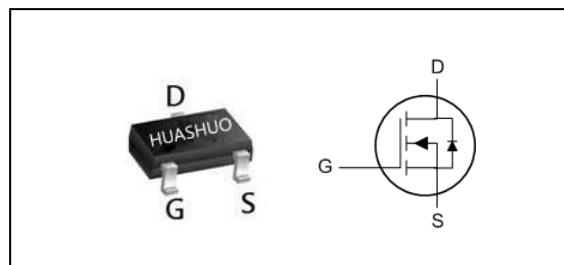
The HSS3N10 meet the RoHS and Green Product requirement with full function reliability approved.

Product Summary

V _{DS}	100	V
R _{DS(ON),typ}	110	mΩ
I _D	3	A

- Green Device Available
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Advanced high cell density Trench technology

SOT23 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	100	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ₁	3	A
I _D @T _A =100°C	Continuous Drain Current, V _{GS} @ 10V ₁	2.4	A
I _{DM}	Pulsed Drain Current ²	12	A
P _D @T _A =25°C	Total Power Dissipation ³	1.2	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient(steady state) ₁	---	104	°C/W
	Thermal Resistance Junction-ambient(t<10s) ₁	---	76	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_{\text{D}}=250\mu\text{A}$	100	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $\text{I}_{\text{D}}=1\text{mA}$	---	0.122	---	$\text{V}/^\circ\text{C}$
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_{\text{D}}=3\text{A}$	---	110	140	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_{\text{D}}=2\text{A}$	---	160	300	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_{\text{D}}=250\mu\text{A}$	1.0	1.8	3.0	V
$\Delta \text{V}_{\text{GS(th)}}$	$\text{V}_{\text{GS(th)}}$ Temperature Coefficient		---	-4.84	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=80\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	10	uA
		$\text{V}_{\text{DS}}=80\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Q_g	Total Gate Charge (10V)	$\text{V}_{\text{DS}}=50\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_{\text{D}}=3\text{A}$	---	4.3	---	nC
Q_{gs}	Gate-Source Charge		---	1.5	---	
Q_{gd}	Gate-Drain Charge		---	1.0	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=50\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_g=2\Omega$ $\text{I}_{\text{D}}=3\text{A}$	---	14.3	---	ns
T_r	Rise Time		---	3.4	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	21	---	
T_f	Fall Time		---	2.8	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=50\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	208	---	pF
C_{oss}	Output Capacitance		---	29	---	
C_{rss}	Reverse Transfer Capacitance		---	1.4	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,4}	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	3	A
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_{\text{S}}=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
trr	Reverse recovery time	$\text{I}_{\text{S}}=3\text{A}, \text{di/dt}=100\text{A/us}$	---	32	---	ns
Qrr	Reverse recovery Charge		---	39	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_{D} and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

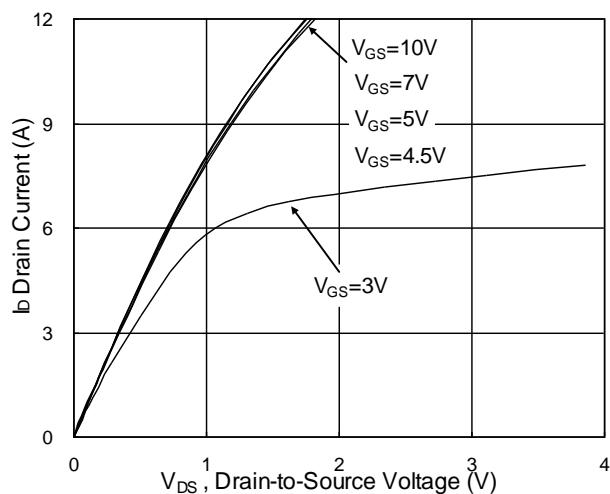


Fig.1 Typical Output Characteristics

N-Ch 100V Fast Switching MOSFETs

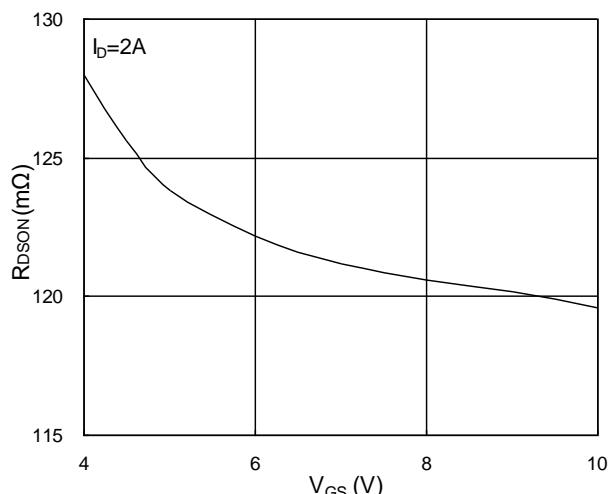


Fig.2 On-Resistance vs. Gate-Source

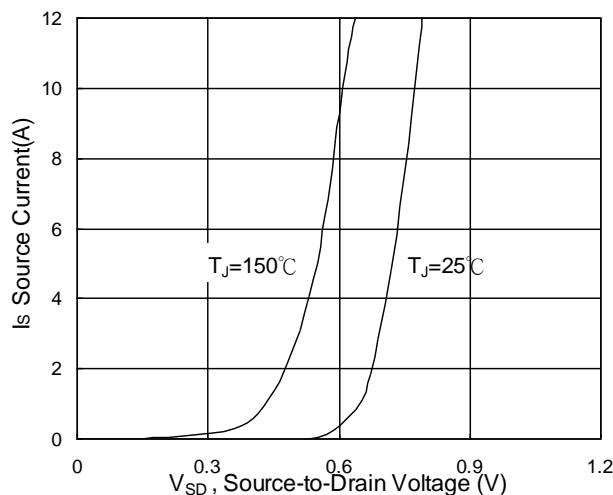


Fig.3 Forward Characteristics Of Reverse

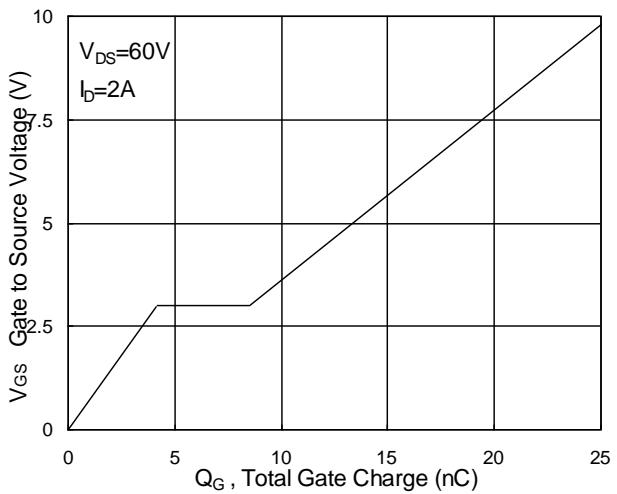


Fig.4 Gate-Charge Characteristics

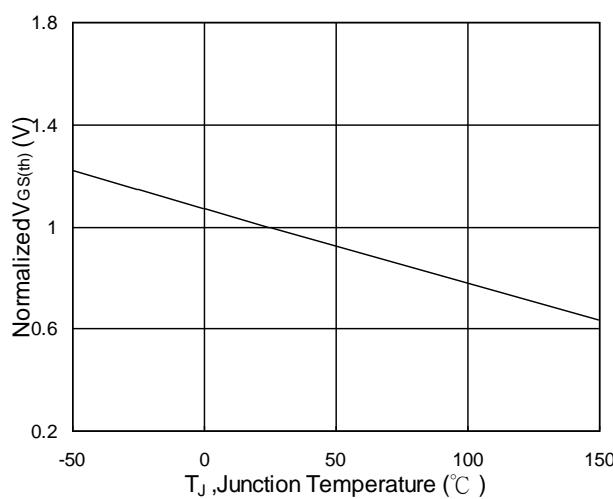


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

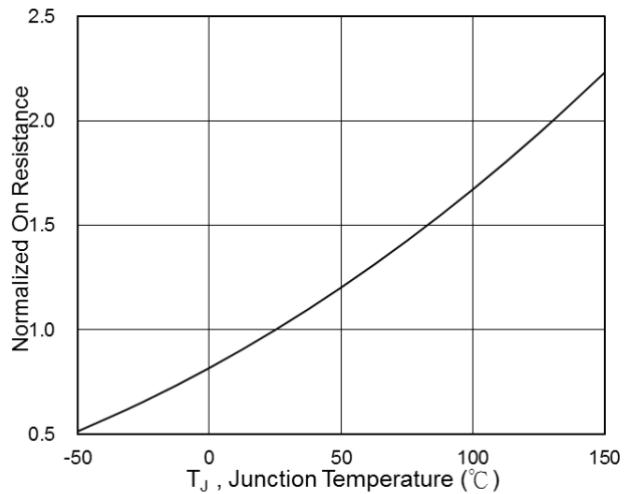


Fig.6 Normalized $R_{DS(on)}$ vs. T_J



HUASHUO
SEMICONDUCTOR

HSS3N10

N-Ch 100V Fast Switching MOSFETs

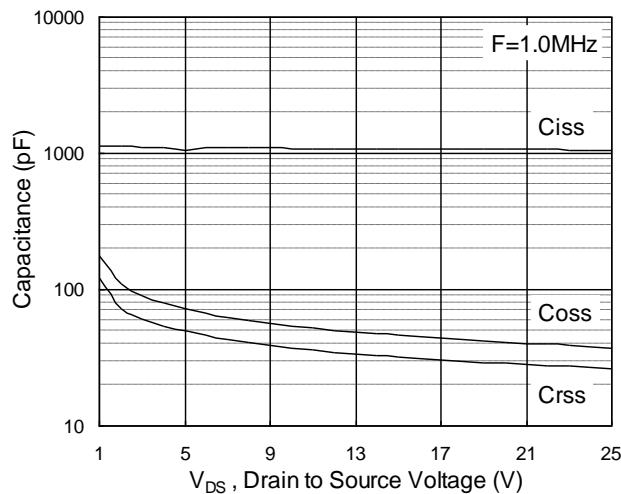


Fig.7 Capacitance

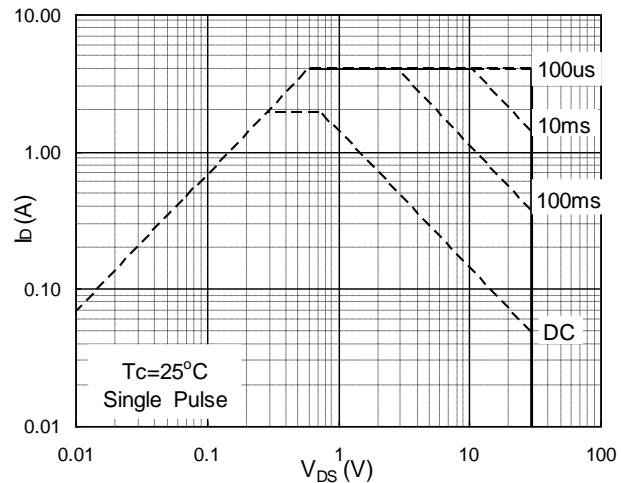


Fig.8 Safe Operating Area

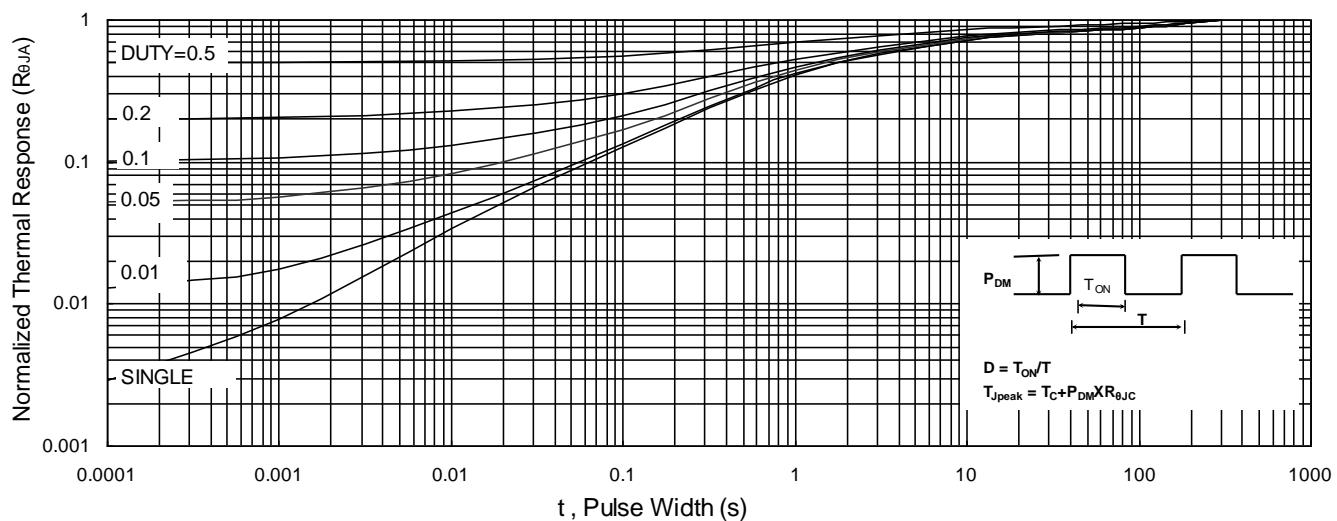


Fig.9 Normalized Maximum Transient Thermal Impedance

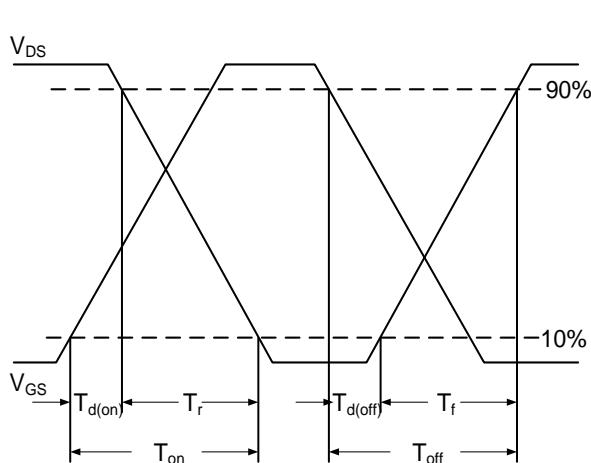


Fig.10 Switching Time Waveform

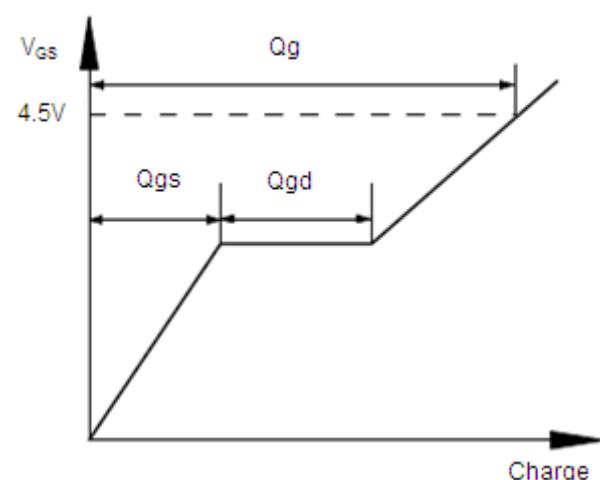
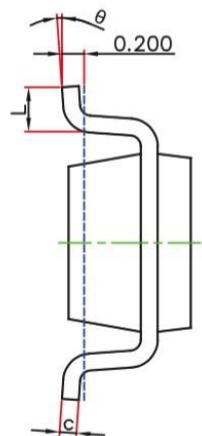
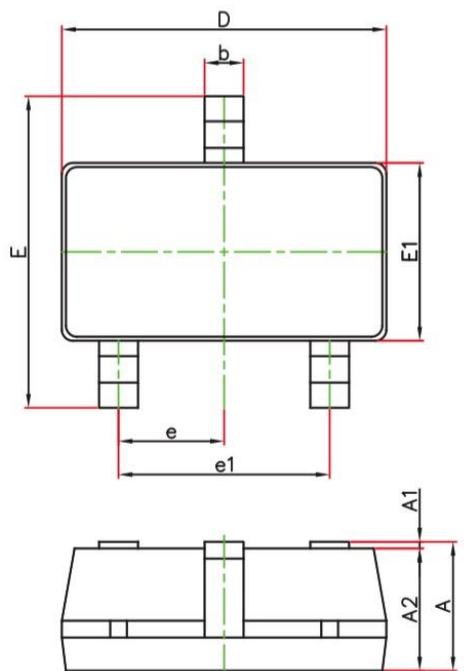


Fig.11 Gate Charge Waveform

Ordering Information

Part Number	Package code	Packaging
HSS3N10	SOT-23L	3000/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°