

# N-Channel 60-V (D-S) MOSFET

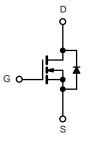
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>		
60	0.012 at $V_{GS}$ = 10 V	60		
	0.013 at $V_{GS}$ = 4.5 V	50		

#### FEATURES

- 175 °C Junction Temperature
- TrenchFET<sup>®</sup> Power MOSFET







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_C$ =	25 °C, unless othe	rwise noted)			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T. 175 °C)h	T <sub>C</sub> = 25 °C	1-	60		
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 100 °C	D D	50 <sup>a</sup>		
Pulsed Drain Current		I <sub>DM</sub>	200	А	
Continuous Source Current (Diode Conduction)		۱ <sub>S</sub>	50 <sup>a</sup>		
Avalanche Current		I <sub>AS</sub>	50	1	
Single Avalanche Energy (Duty Cycle $\leq$ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ	
Maniarum Davies Dissis ation	T <sub>C</sub> = 25 °C	Pn	136	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		3 <sup>b</sup> , 8.3 <sup>b, c</sup>	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum hungting to Ambient	$t \le 10 \text{ sec}$	R <sub>thJA</sub>	15	18	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		40	50	
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t  $\leq$  10 s.

Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1	2	3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50		
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 \text{ °C}$			250	1	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	60			А	
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.010	0.012	Ω	
	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C			0.016		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C			0.020		
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A			0.013		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		60		S	
Dynamic	•			•			
Input Capacitance	C <sub>iss</sub>			2650		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		470			
Reverse Transfer Capacitance	C <sub>rss</sub>			225			
Total Gate Charge <sup>c</sup>	Qg			47	70		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 50 \text{ A}$		10		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 0.6 \Omega$ $\text{I}_{D} \cong 50 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{g} = 2.5 \Omega$		15	25	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			35	50		
Fall Time <sup>c</sup>	t <sub>f</sub>			20	30		
Source-Drain Diode Ratings and Cha	racteristics (	T <sub>C</sub> = 25 °C)		•			
Pulsed Current	I <sub>SM</sub>				60	А	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>E</sub> = 20 A, di/dt = 100 A/µs		45	100	ns	

Notes:

a. For design aid only; not subject to production testing.

b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

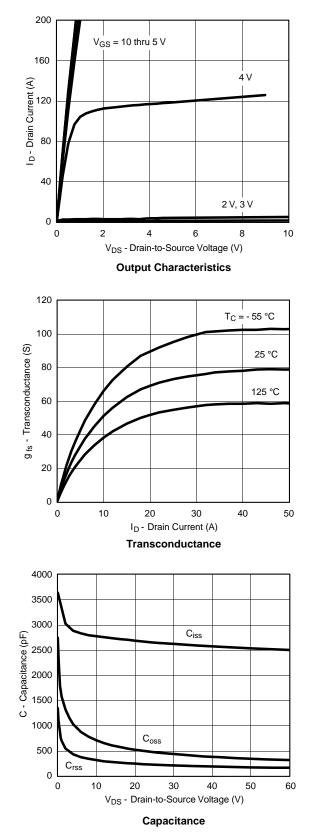
c. Independent of operating temperature.

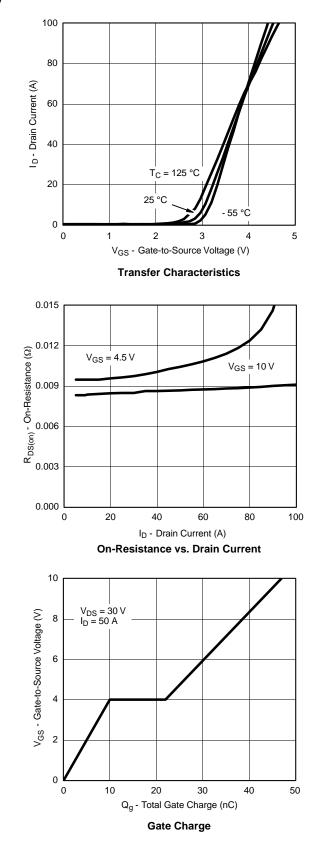
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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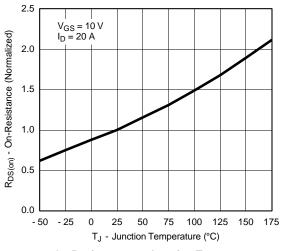
#### TYPICAL CHARACTERISTICS (25 °C unless noted)



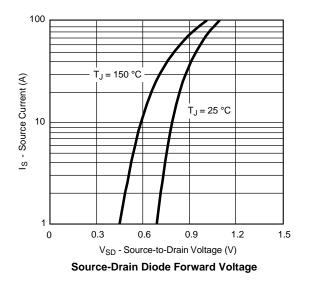




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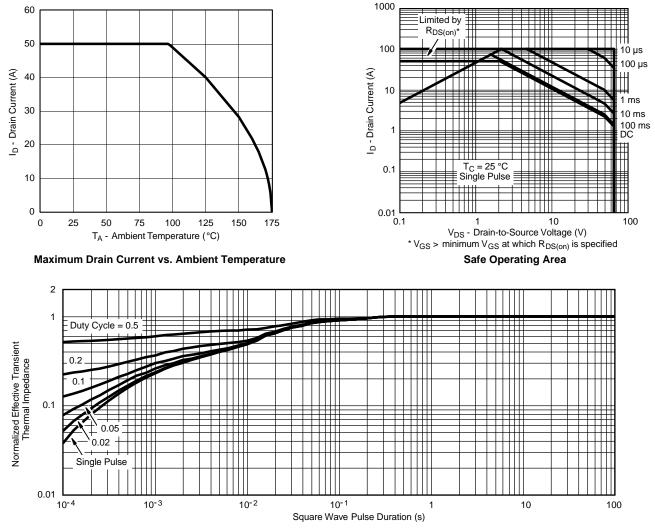
**On-Resistance vs. Junction Temperature** 



### IPB120N06NG

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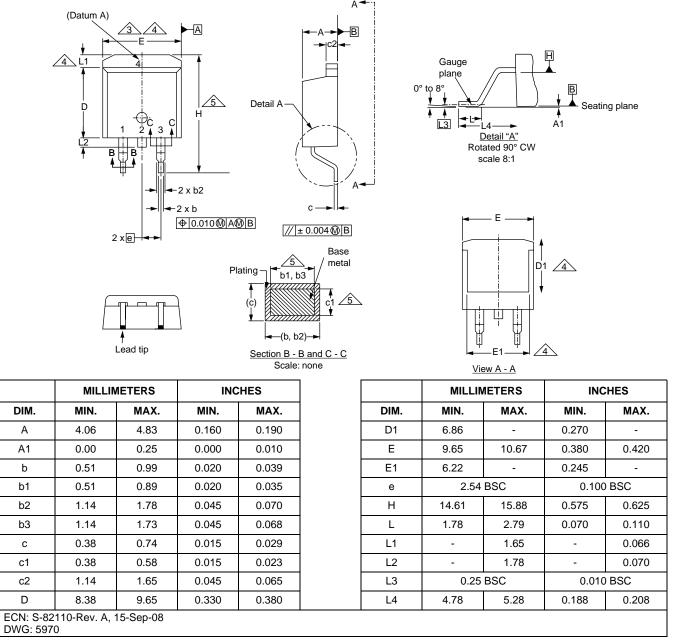
#### **THERMAL RATINGS**



Normalized Thermal Transient Impedance, Junction-to-Case



#### **TO-263AB (HIGH VOLTAGE)**



#### Notes

1. Dimensioning and tolerancing per ASME Y14.5M-1994.

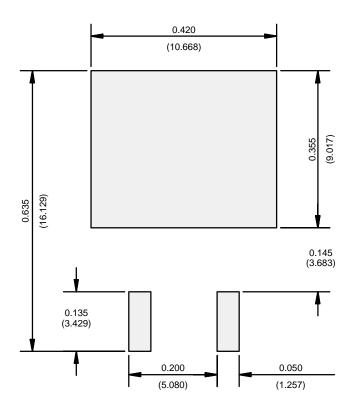
2. Dimensions are shown in millimeters (inches).

3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.

- 4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
- 5. Dimension b1 and c1 apply to base metal only.
- 6. Datum A and B to be determined at datum plane H.
- 7. Outline conforms to JEDEC outline to TO-263AB.



#### **RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)



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